

# Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh  
I B.Tech II Semester (SVEC-16) Mid-II Examinations, May-2018

## TIME - TABLE

Date : 30/04/2018

Time : 10:30 AM TO 12:30 PM

EXAM DATE & TIME	WEEK DAY	BTECH-CE	BTECH-CSE	BTECH-CSSE	BTECH-ECE	BTECH-EEE	BTECH-EIE	BTECH-IT	BTECH-ME
14/05/2018 10:30AM TO 12:30PM	MONDAY	ENGINEERING PHYSICS [16BT1BS02]	ENGINEERING PHYSICS [16BT1BS02]	ENGINEERING PHYSICS [16BT1BS02]	TECHNICAL ENGLISH [16BT1HS01]	TECHNICAL ENGLISH [16BT1HS01]	TECHNICAL ENGLISH [16BT1HS01]	ENGINEERING PHYSICS [16BT1BS02]	ENGINEERING PHYSICS [16BT1BS02]
15/05/2018 10:30AM TO 12:30PM	TUESDAY	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]
16/05/2018 10:30AM TO 12:30PM	WEDNESDAY	BUILDING MATERIAL AND CONSTRUCTION TECHNOLOGY [16BT20101]	BASIC ELECTRONIC DEVICES AND CIRCUITS [16BT20441]	BASIC ELECTRONIC DEVICES AND CIRCUITS [16BT20441]	ELECTRONIC DEVICES AND CIRCUITS [16BT20401]	ELECTRONIC DEVICES AND CIRCUITS [16BT20401]	ELECTRONIC DEVICES AND CIRCUITS [16BT20401]	BASIC ELECTRONIC DEVICES AND CIRCUITS [16BT20441]	ENGINEERING MATERIALS [16BT20301]
17/05/2018 10:30AM TO 12:30PM	THURSDAY	ENGINEERING MECHANICS [16BT20102]	OBJECT ORIENTED PROGRAMMING THROUGH C++ [16BT21201]	OBJECT ORIENTED PROGRAMMING THROUGH C++ [16BT21201]	ENGINEERING CHEMISTRY [16BT1BS01]	ENGINEERING CHEMISTRY [16BT1BS01]	ENGINEERING CHEMISTRY [16BT1BS01]	OBJECT ORIENTED PROGRAMMING THROUGH C++ [16BT21201]	ENGINEERING MECHANICS [16BT20102]
18/05/2018 10:30AM TO 12:30PM	FRIDAY	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING [16BT20241]	DIGITAL LOGIC DESIGN [16BT21501]	DIGITAL LOGIC DESIGN [16BT21501]	FOUNDATIONS OF DATA STRUCTURES [16BT20541]	FOUNDATIONS OF DATA STRUCTURES [16BT20541]	FOUNDATIONS OF DATA STRUCTURES [16BT20541]	DIGITAL LOGIC DESIGN [16BT21501]	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING [16BT20241]

1. Students have to bring their College Identity Cards to the examination hall; otherwise they will not be permitted to write the examination.
2. Examination will be on the Units prescribed in the Syllabus Book.
3. College buses will leave the college with in 15 minutes after Examination Time.

  
**PRINCIPAL**  


- Copy to: 1) The Director, SVEC.  
 2) The HODs of CE, EEE, ME, ECE, CSE, EIE, IT, CSSE & BS&H: with a request to arrange to read out in the respective class rooms and circulation among the staff and to display on notice board.  
 3) The Examination Section.  
 4) The S.A.O, for information and necessary transport arrangement.