SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

M.Tech II Semester (SVEC-16) Regular Examinations, October 2018 TIME - TABLE

Date: 07/09/2018

Time: 02:00 PM TO 05:00 PM

| | | | | | | 711110 . 02.1 | 00 FW 10 03.00 FW |
|--------------------------------|-----------|-------------------------------------|-------------------------------------|--|---|---|---|
| EXAM DATE & TIME | WEEK DAY | MTECH-CMS | MTECH-CS | MTECH-DECS | MTECH-EPS | MTECH-PED | MTECH-VLSI |
| 29/10/2018 2:00PM TO 5:00PM | MONDAY | DETECTION AND ESTIMATION OF SIGNALS | ADVANCED COMPUTER ARCHITECTURE | DETECTION AND ESTIMATION OF SIGNALS | FLEXIBLE AC TRANSMISSION SYSTEM | LINEAR AND NONLINEAR CONTROL SYSTEMS | LOW POWER VLSI DESIGN |
| | | [16MT23801] | [16MT20501] | [16MT23801] | [16MT20701] | [16MT28301] | [16MT25701] |
| 31/10/2018 2:00PM TO 5:00PM | WEDNESDAY | ADAPTIVE SIGNAL PROCESSING | BIG DATA ANALYTICS | EMBEDDED SYSTEM DESIGN | INTELLIGENT SYSTEMS | POWER ELECTRONICS IN RENEWABLE ENERGY SYSTEMS | MIXED SIGNAL DESIGN |
| | | [16MT26101] | [16MT20502] | [16MT23802] | [16MT20702] | [16MT28302] | [16MT25702] |
| 02/11/2018 2:00PM TO 5:00PM | FRIDAY | IMAGE & VIDEO PROCESSING | OBJECT ORIENTED ANALYSIS AND DESIGN | INFORMATION THEORY AND CODING TECHNIQUES | POWER SYSTEM STABILITY AND CONTROL | SOLID STATE AC DRIVES | NANOELECTRONICS |
| | | [16MT13804] | [16MT20503] | [16MT23803] | [16MT20703] | [16MT28303] | [16MT25703] |
| 06/11/2018 2:00PM TO 5:00PM | TUESDAY | SMART ANTENNAS | CLOUD COMPUTING | LOW POWER CMOS VLSI DESIGN | RESTRUCTURED POWER SYSTEM | SOLID STATE DC DRIVES | PHYSICAL DESIGN AUTOMATION |
| | | [16MT26102] | [16MT12501] | [16MT23804] | [16MT20704] | [16MT28304] | [16MT25704] |
| 09/11/2018 2:00PM TO 5:00PM | FRIDAY | WIRELESS COMMUNICATIONS | WEB TECHNOLOGIES | WIRELESS COMMUNICATIONS | STATIC AND DIGITAL PROTECTION OF POWER SYSTEM | SPECIAL ELECTRICAL MACHINES | TESTING AND TESTABILITY |
| | | [16MT23805] | [16MT22505] | [16MT23805] | [16MT20705] | [16MT28305] | [16MT25705] |
| 12/11/2018 2:00PM TO 5:00PM | MONDAY | WIRELESS SENSOR NETWORKS | INFORMATION SECURITY | OPTICAL COMMUNICATIONS AND NETWORKS | POWER QUALITY | POWER QUALITY | SYSTEM-ON-CHIP DESIGN AND VERIFICATION |
| | | [16MT25709] | [16MT20505] | [16MT23807] | [16MT20708] | [16MT20708] | [16MT25708] |

Copy to: 1) The Director, SVEC.

2) The HODs of EEE, ECE, CSE & IT: with a request to arrange to read out in the respective class rooms and circulation among the staff and to display on notice board.

3) The Examination Section.

4) The S.A.O, for information and necessary transport arrangement.

HIEF CONTROLLER OF EXAMINATIONS