

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

M.Tech I Semester (SVEC-14) Supplementary Examinations, October 2018

TIME - TABLE

Date : 07/09/2018

Time : 02:00 PM TO 05:00 PM

EXAM DATE & TIME	WEEK DAY	MTECH-CMS	MTECH-CNIS	MTECH-CS	MTECH-DECS	MTECH-EPS	MTECH-SE	MTECH-VLSI
30/10/2018 2:00PM TO 5:00PM	TUESDAY	ADVANCED DIGITAL SIGNAL PROCESSING [14MT15706]	ADVANCED COMPUTER NETWORKS [14MT10501]	ADVANCED COMPUTER NETWORKS [14MT10501]	COMPUTER ARCHITECTURES [14MT13801]	POWER SYSTEM SECURITY AND STATE ESTIMATION [14MT10701]	SOFTWARE DEVELOPMENT METHODOLOGIES [14MT12501]	ANALOG IC DESIGN [14MT15701]
01/11/2018 2:00PM TO 5:00PM	THURSDAY	DIGITAL COMMUNICATION TECHNIQUES [14MT13802]	INFORMATION SECURITY [14MT20507]	ADVANCED DATABASE MANAGEMENT SYSTEMS [14MT10502]	DIGITAL COMMUNICATION TECHNIQUES [14MT13802]	STATIC AND DIGITAL PROTECTION OF POWER SYSTEM [14MT10702]	SOFTWARE PROCESS AND PROJECT MANAGEMENT [14MT12502]	COMPUTATIONAL TECHNIQUES IN MICROELECTRONICS [14MT15702]
05/11/2018 2:00PM TO 5:00PM	MONDAY	COMPUTER NETWORKS [14MT13805]	MOBILE COMPUTING [14MT20508]	ADVANCED SOFTWARE ENGINEERING [14MT10503]	DIGITAL SYSTEM DESIGN [14MT13803]	ADVANCED POWER SYSTEM STABILITY ANALYSIS [14MT10703]	SOFTWARE REQUIREMENTS AND ESTIMATION [14MT12503]	DEVICE MODELING [14MT15703]
08/11/2018 2:00PM TO 5:00PM	THURSDAY	LINEAR ALGEBRA [14MT13809]	NETWORK MANAGEMENT [14MT16301]	DATA STRUCTURES AND ALGORITHMS [14MT10504]	LINEAR ALGEBRA [14MT13809]	POWER ELECTRONIC CONVERTERS [14MT10704]	DATA STRUCTURES AND ALGORITHMS [14MT10504]	DIGITAL IC DESIGN [14MT15704]
10/11/2018 2:00PM TO 5:00PM	SATURDAY	OPTICAL COMMUNICATIONS AND NETWORKS [14MT23808]	WIRELESS NETWORKS [14MT16302]	DISCRETE STRUCTURES AND GRAPH THEORY [14MT10505]	MODERN DIGITAL SIGNAL PROCESSING [14MT13804]	ADVANCED CONTROL SYSTEMS [14MT10705]	WEB TECHNOLOGIES [14MT12504]	IC FABRICATION [14MT15705]
13/11/2018 2:00PM TO 5:00PM	TUESDAY	SATELLITE COMMUNICATIONS [14MT16102]	DATA WAREHOUSING AND DATA MINING [14MT20503]	SOFTWARE PROJECT MANAGEMENT [14MT10509]	EMBEDDED SYSTEM DESIGN [14MT13807]	REACTIVE POWER COMPENSATION AND MANAGEMENT [14MT10707]	CLOUD COMPUTING [14MT12508]	LOW VOLTAGE ANALOG CIRCUIT DESIGN [14MT15708]
15/11/2018 2:00PM TO 5:00PM	THURSDAY	RESEARCH METHODOLOGY [14MT10310]	RESEARCH METHODOLOGY [14MT10310]	RESEARCH METHODOLOGY [14MT10310]	RESEARCH METHODOLOGY [14MT10310]	RESEARCH METHODOLOGY [14MT10310]	RESEARCH METHODOLOGY [14MT10310]	RESEARCH METHODOLOGY [14MT10310]


CHIEF CONTROLLER OF EXAMINATIONS

Copy to: 1) The Director, SVEC.

2) The HODs of EEE, ECE, CSE & IT: with a request to arrange to read out in the respective class rooms and circulation among the staff and to display on notice board.

3) The Examination Section.

4) The S.A.O, for information and necessary transport arrangement.