Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati,Chittoor,Andhra Pradesh

M.Tech II Semester (SVEC-14) Supplementary Examinations, October 2018

TIME - TABLE

Date : 11/09/2018

Time : 02:00 PM TO 05:00 PM

EXAM DATE & TIME	WEEK DAY	MTECH-CMS	MTECH-CNIS	MTECH-CS	MTECH-DECS	MTECH-EPS	MTECH-SE	MTECH-VLSI
29/10/2018 2:00PM TO 5:00PM	MONDAY	ADAPTIVE SIGNAL PROCESSING [14MT26101]	AD-HOC WIRELESS NETWORKS [14MT26301]	ADVANCED COMPUTER ARCHITECTURE [14MT20501]	DISPLAY TECHNOLOGIES AND DEVICES [14MT23801]	OPERATION AND CONTROL OF POWER SYSTEM [14MT20701]	SERVICE ORIENTED ARCHITECTURE [14MT22501]	PHYSICAL DESIGN AUTOMATION [14MT25701]
31/10/2018 2:00PM TO 5:00PM	WEDNESDAY	INFORMATION THEORY AND CODING TECHNIQUES [14MT23802]	COMPUTER FORENSICS [14MT26302]	ADVANCED OPERATING SYSTEMS [14MT20502]	INFORMATION THEORY AND CODING TECHNIQUES [14MT23802]	FLEXIBLE AC TRANSMISSION SYSTEM [14MT20702]	SOFTWARE ARCHITECTURE & DESIGN PATTERNS [14MT22502]	LOW POWER VLSI DESIGN [14MT25702]
02/11/2018 2:00PM TO 5:00PM	FRIDAY	DETECTION AND ESTIMATION OF SIGNALS [14MT23806]	INTRUSION DETECTION SYSTEMS [14MT26303]	DATA WAREHOUSING AND DATA MINING [14MT20503]	MICROCONTROLLER BASED SYSTEM DESIGN [14MT23803]	POWER QUALITY [14MT20703]	SOFTWARE MEASUREMENT AND METRICS [14MT22503]	MIXED SIGNAL DESIGN [14MT25703]
06/11/2018 2:00PM TO 5:00PM	TUESDAY	SOFTWARE DEFINED RADIO [14MT26102]	VIRTUALIZATION AND CLOUD COMPUTING [14MT20509]	OBJECT ORIENTED ANALYSIS AND DESIGN [14MT20504]	TESTING AND TESTABILITY OF DIGITAL SYSTEMS [14MT23804]	SMART GRID TECHNOLOGY [14MT20704]	SOFTWARE SECURITY ENGINEERING [14MT22504]	RF IC DESIGN [14MT25704]
09/11/2018 2:00PM TO 5:00PM	FRIDAY	WIRELESS COMMUNICATIONS [14MT23805]	WEB PROGRAMMING [14MT20505]	WEB PROGRAMMING [14MT20505]	WIRELESS COMMUNICATIONS [14MT23805]	INTELLIGENT CONTROL [14MT20705]	SOFTWARE TESTING [14MT22505]	TESTING AND TESTABILITY [14MT25705]
12/11/2018 2:00PM TO 5:00PM	MONDAY	RADAR SIGNAL PROCESSING [14MT26104]	TCP/IP PROTOCOLS [14MT26306]	MOBILE COMPUTING [14MT20508] BIG DATA ANALYTICS [14MT20506]	OPTICAL COMMUNICATIONS AND NETWORKS [14MT23808]	POWER SYSTEM RELIABILITY [14MT20708] ENERGY AUDIT CONSERVATION AND MANAGEMENT [14MT20709]	BIG DATA TECHNOLOGIES [14MT22506]	ASIC DESIGN [14MT25706] CO-DESIGN [14MT25707]

CHIEF CONTROLLER OF EXAMINATIONS

Copy to: 1) The Director, SVEC.
2) The HODs of EEE, ECE, CSE & IT: with a request to arrange to read out in the respective class rooms and circulation among the staff and to display on notice board.
3) The Examination Section.
4) The S.A.O, for information and necessary transport arrangement.