

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

II B.Tech I Semester (SVEC-16) Regular Examinations, November 2018

TIME - TABLE

Date : 26/09/2018

Time : 10:00 AM TO 01:00 PM

EXAM DATE & TIME	WEEK DAY	BTECH-CE	BTECH-CSE	BTECH-CSSE	BTECH-ECE	BTECH-EEE	BTECH-EIE	BTECH-IT	BTECH-ME
19/11/2018 10:00 AM To 1:00 PM	MONDAY	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]
22/11/2018 10:00 AM To 1:00 PM	THURSDAY	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY [16BT3HS02]	COMPUTER ORGANIZATION [16BT30501]	COMPUTER ORGANIZATION [16BT30501]	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS [16BT3BS02]	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS [16BT3BS02]	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS [16BT3BS02]	COMPUTER ORGANIZATION [16BT30501]	ENGINEERING METALLURGY [16BT30301]
24/11/2018 10:00 AM To 1:00 PM	SATURDAY	CONSTRUCTION PLANNING AND PROJECT MANAGEMENT [16BT30101]	DATA STRUCTURES [16BT30502]	DATA STRUCTURES [16BT30502]	ELECTRONIC CIRCUIT ANALYSIS & DESIGN [16BT30401]	DC MACHINES [16BT30201]	ELECTRICAL AND ELECTRONIC MEASUREMENTS [16BT31001]	DATA STRUCTURES [16BT30502]	KINEMATICS OF MACHINERY [16BT30302]
27/11/2018 10:00 AM To 1:00 PM	TUESDAY	FLUID MECHANICS AND HYDRAULIC MACHINERY [16BT30102]	DISCRETE MATHEMATICAL STRUCTURES [16BT31201]	DISCRETE MATHEMATICAL STRUCTURES [16BT31201]	SIGNALS AND SYSTEMS [16BT30402]	ELECTROMAGNETIC FIELDS [16BT30202]	SENSORS AND TRANSDUCERS [16BT31002]	DISCRETE MATHEMATICAL STRUCTURES [16BT31201]	MANUFACTURING TECHNOLOGY [16BT30303]
29/11/2018 10:00AM TO 1:00PM	THURSDAY	MECHANICS OF SOLIDS [16BT30103]	PYTHON PROGRAMMING [16BT30503]	JAVA PROGRAMMING [16BT41202]	SWITCHING THEORY AND LOGIC DESIGN [16BT30403]	SIGNALS, SYSTEMS AND NETWORKS [16BT30203]	SWITCHING THEORY AND LOGIC DESIGN [16BT30403]	LINUX PROGRAMMING [16BT50502]	STRENGTH OF MATERIALS [16BT30304]
03/12/2018 10:00 AM To 1:00 PM	MONDAY	SURVEYING [16BT30104]	OPERATING SYSTEMS [16BT31501]	OPERATING SYSTEMS [16BT31501]	ELECTRICAL TECHNOLOGY [16BT30241]	ANALOG ELECTRONIC CIRCUITS [16BT30441]	ELECTRICAL TECHNOLOGY [16BT30241]	OPERATING SYSTEMS [16BT31501]	THERMODYNAMICS [16BT30305]

Copy to : The Director, SVEC


CHIEF CONTROLLER OF EXAMINATIONS

The HODs, of CE, ME, EEE, ECE, CSE, EIE, IT & CSSE: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board; The S.A.O, S.V.E.C; The Examination Section; The Manager, SVEC Canteen; Estate Manager; Hostel Notice Boards.