

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

II B.Tech II Semester (SVEC-16) Supplementary Examinations, December 2018

TIME - TABLE

Date : 26/09/2018

Time : 02:00 PM TO 05:00 PM

EXAM DATE & TIME	WEEK DAY	BTECH-CE	BTECH-CSE	BTECH-CSSE	BTECH-ECE	BTECH-EEE	BTECH-EIE	BTECH-IT	BTECH-ME
06/12/2018 2:00 PM To 5:00 PM	THURSDAY	ENVIRONMENTAL STUDIES [16BT3HS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	ANALOG COMMUNICATIONS [16BT40401]	ELECTRICAL MEASUREMENTS [16BT40201]	CONTROL SYSTEMS [16BT50201]	ENVIRONMENTAL STUDIES [16BT3HS01]	ENVIRONMENTAL STUDIES [16BT3HS01]
08/12/2018 2:00 PM To 5:00 PM	SATURDAY	CONCRETE TECHNOLOGY [16BT40101]	COMPUTER GRAPHICS [16BT40501]	THEORY OF COMPUTATION [16BT41204]	DIGITAL IC APPLICATIONS [16BT40402]	GENERATION OF ELECTRIC POWER [16BT40202]	ELECTRONIC CIRCUIT ANALYSIS AND DESIGN [16BT30401]	THEORY OF COMPUTATION [16BT41204]	DESIGN OF MACHINE ELEMENTS-I [16BT40301]
11/12/2018 2:00 PM To 5:00 PM	TUESDAY	ENGINEERING GEOLOGY [16BT40102]	DATABASE MANAGEMENT SYSTEMS [16BT40502]	DATABASE MANAGEMENT SYSTEMS [16BT40502]	ELECTROMAGNETIC THEORY AND TRANSMISSION LINES [16BT40403]	TRANSFORMERS AND INDUCTION MACHINES [16BT40203]	INDUSTRIAL INSTRUMENTATION-I [16BT41001]	DATABASE MANAGEMENT SYSTEMS [16BT40502]	DYNAMICS OF MACHINERY [16BT40302]
13/12/2018 2:00 PM To 5:00 PM	THURSDAY	ENGINEERING HYDROLOGY [16BT40103]	DESIGN AND ANALYSIS OF ALGORITHMS [16BT41201]	DESIGN AND ANALYSIS OF ALGORITHMS [16BT41201]	LINEAR IC APPLICATIONS [16BT40404]	LINEAR AND DIGITAL ICS [16BT41002]	LINEAR AND DIGITAL ICS [16BT41002]	DESIGN AND ANALYSIS OF ALGORITHMS [16BT41201]	FLUID MECHANICS [16BT40303]
15/12/2018 2:00 PM To 5:00 PM	SATURDAY	STRUCTURAL ANALYSIS-I [16BT40104]	JAVA PROGRAMMING [16BT41202]	OBJECT ORIENTED ANALYSIS AND DESIGN [16BT51202]	PROBABILITY AND STOCHASTIC PROCESS [16BT40405]	SWITCHING THEORY AND LOGIC DESIGN [16BT30403]	SIGNALS AND SYSTEMS [16BT30402]	JAVA PROGRAMMING [16BT41202]	MACHINE TOOLS AND MODERN MACHINING PROCESS [16BT40304]
18/12/2018 2:00 PM To 5:00 PM	TUESDAY	WATER SUPPLY ENGINEERING [16BT40105]	SOFTWARE ENGINEERING [16BT41203]	SOFTWARE ENGINEERING [16BT41203]	PULSE AND DIGITAL CIRCUITS [16BT40406]	COMPUTER ARCHITECTURE AND ORGANIZATION [16BT41041]	PULSE AND DIGITAL CIRCUITS [16BT40406]	SOFTWARE ENGINEERING [16BT41203]	THERMAL ENGINEERING - I [16BT40305]


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC

The HODs, of CE, ME, EEE, ECE, CSE, EIE, IT & CSSE: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board; The S.A.O, S.V.E.C; The Examination Section; The Manager, SVEC Canteen; Estate Manager; Hostel Notice Boards.