

## Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh  
M.Tech I Semester (SVEC-16) Supplementary Examinations, January 2019

### TIME - TABLE

Date : 05/12/2018

Time : 02:00 PM TO 05:00 PM

| EXAM DATE & TIME               | WEEK DAY  | MTECH-CMS   | MTECH-CNIS                                      | MTECH-CS  | MTECH-DECS                                       | MTECH-EPS  | MTECH-PED   | MTECH-SE  | MTECH-VLSI   |
|--------------------------------|-----------|---|---|---|--|--|---|---|--|
| 21/01/2019<br>2:00PM TO 5:00PM | MONDAY    | ADVANCED DIGITAL SIGNAL PROCESSING<br>[16MT15706]       | ADVANCED COMPUTER NETWORKS<br>[16MT10501]       | ADVANCED COMPUTER NETWORKS<br>[16MT10501]           | COMPUTER NETWORKS<br>[16MT13801]                 | ADVANCED CONTROL SYSTEMS<br>[16MT10701]  | APPLIED MATHEMATICS<br>[16MT18501]                  | CLOUD COMPUTING<br>[16MT12501]                      | ANALOG IC DESIGN<br>[16MT15701]                          |
| 23/01/2019<br>2:00PM TO 5:00PM | WEDNESDAY | DIGITAL COMMUNICATION TECHNIQUES<br>[16MT13802]         | INFORMATION SECURITY<br>[16MT20505]             | ADVANCED DATABASE MANAGEMENT SYSTEMS<br>[16MT10502] | DIGITAL COMMUNICATION TECHNIQUES<br>[16MT13802]  | HIGH VOLTAGE ENGINEERING<br>[16MT10702]  | ADVANCED POWER SEMICONDUCTOR DEVICES<br>[16MT18301] | SOFTWARE DEVELOPMENT METHODOLOGIES<br>[16MT12503]   | COMPUTATIONAL METHODS IN MICROELECTRONICS<br>[16MT15702] |
| 25/01/2019<br>2:00PM TO 5:00PM | FRIDAY    | INFORMATION THEORY AND CODING TECHNIQUES<br>[16MT23803] | MOBILE COMPUTING<br>[16MT20506]                 | ADVANCED OPERATING SYSTEMS<br>[16MT10503]           | DIGITAL SYSTEM DESIGN AND TESTING<br>[16MT13803] | POWER ELECTRONIC CONVERTERS<br>[16MT10703]   | ANALYSIS OF INVERTERS<br>[16MT18302]                | SOFTWARE MEASUREMENT AND METRICS<br>[16MT12504]     | DEVICE MODELING<br>[16MT15703]                           |
| 29/01/2019<br>2:00PM TO 5:00PM | TUESDAY   | OPTICAL COMMUNICATIONS AND NETWORKS<br>[16MT23807]      | DATA WAREHOUSING AND DATA MINING<br>[16MT10504] | DATA WAREHOUSING AND DATA MINING<br>[16MT10504]     | IMAGE & VIDEO PROCESSING<br>[16MT13804]          | POWER SYSTEM SECURITY AND STATE ESTIMATION<br>[16MT10704]                                    | ANALYSIS OF POWER CONVERTERS<br>[16MT18303]         | SOFTWARE REQUIREMENTS AND ESTIMATION<br>[16MT12505] | DIGITAL IC DESIGN<br>[16MT15704]                         |
| 31/01/2019<br>2:00PM TO 5:00PM | THURSDAY  | RF CIRCUIT DESIGN<br>[16MT16101]                        | WEB TECHNOLOGIES<br>[16MT22505]                 | DATA STRUCTURES AND ALGORITHMS<br>[16MT12502]       | MODERN DIGITAL SIGNAL PROCESSING<br>[16MT13805]  | REACTIVE POWER COMPENSATION AND MANAGEMENT<br>[16MT10705]                                    | MODELLING OF ELECTRICAL MACHINES<br>[16MT18304]     | DATA STRUCTURES AND ALGORITHMS<br>[16MT12502]       | IC FABRICATION<br>[16MT15705]                            |
| 04/02/2019<br>2:00PM TO 5:00PM | MONDAY    | DIGITAL SATELLITE COMMUNICATIONS<br>[16MT16102]         | INTERNET OF THINGS<br>[16MT10507]               | SOFTWARE TESTING TECHNIQUES<br>[16MT22504]          | ASIC DESIGN<br>[16MT13806]                       | MICROCONTROLLER S AND APPLICATIONS<br>[16MT10707]<br>POWER SYSTEM RELIABILITY<br>[16MT10708] | MICROCONTROLLER S AND APPLICATIONS<br>[16MT10707]   | USER INTERFACE DESIGN<br>[16MT12509]                | REAL TIME SYSTEMS<br>[16MT23808]                         |

CHIEF CONTROLLER OF EXAMINATIONS

Copy to: 1) The Director, SVEC.

2) The HODs of EEE, ECE, CSE & IT: with a request to arrange to read out in the respective class rooms and circulation among the staff and to display on notice board.

3) The Examination Section.

4) The S.A.O, for information and necessary transport arrangement.

ACE-3  
for N/A  
05/12/18