

Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

M.Tech II Semester (SVEC-16) Regular Examinations, August 2019

TIME - TABLE

Date : 20/06/2019

Time : 02:00 PM TO 05:00 PM

EXAM DATE & TIME	WEEK DAY	MTECH-CS	MTECH-EPS	MTECH-VLSI
13/08/2019 2:00PM TO 5:00PM	TUESDAY	ADVANCED COMPUTER ARCHITECTURE [16MT20501]	FLEXIBLE AC TRANSMISSION SYSTEM [16MT20701]	LOW POWER VLSI DESIGN [16MT25701]
16/08/2019 2:00PM TO 5:00PM	FRIDAY	BIG DATA ANALYTICS [16MT20502]	INTELLIGENT SYSTEMS [16MT20702]	MIXED SIGNAL DESIGN [16MT25702]
19/08/2019 2:00PM TO 5:00PM	MONDAY	OBJECT ORIENTED ANALYSIS AND DESIGN [16MT20503]	POWER SYSTEM STABILITY AND CONTROL [16MT20703]	NANOELECTRONICS [16MT25703]
21/08/2019 2:00PM TO 5:00PM	WEDNESDAY	CLOUD COMPUTING [16MT12501]	RESTRUCTURED POWER SYSTEM [16MT20704]	PHYSICAL DESIGN AUTOMATION [16MT25704]
24/08/2019 2:00PM TO 5:00PM	SATURDAY	WEB TECHNOLOGIES [16MT22505]	STATIC AND DIGITAL PROTECTION OF POWER SYSTEM [16MT20705]	TESTING AND TESTABILITY [16MT25705]
27/08/2019 2:00PM TO 5:00PM	TUESDAY	INFORMATION SECURITY [16MT20505]	POWER QUALITY [16MT20708]	ASIC DESIGN [16MT13806]


CHIEF CONTROLLER OF EXAMINATIONS

Copy to: 1) The Director, SVEC.

- 2) The HODs of EEE, ECE, CSE: with a request to arrange to read out in the respective class rooms and circulation among the staff and to display on notice board.
- 3) The Examination Section
- 4) The S.A.O, for information and necessary transport arrangements