

Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

M.Tech II Semester (SVEC 16) Mid-II Examinations, July 2019

TIME - TABLE

Date : 08/07/2019

Time : 10:00 AM TO 12:00 PM

EXAM DATE & TIME	WEEK DAY	MTECH-CS	MTECH-EPS	MTECH-VLSI
22/07/2019 10:00 AM TO 12:00 PM	MONDAY	ADVANCED COMPUTER ARCHITECTURE [16MT20501]	FLEXIBLE AC TRANSMISSION SYSTEM [16MT20701]	LOW POWER VLSI DESIGN [16MT25701]
23/07/2019 10:00 AM TO 12:00 PM	TUESDAY	BIG DATA ANALYTICS [16MT20502]	INTELLIGENT SYSTEMS [16MT20702]	MIXED SIGNAL DESIGN [16MT25702]
24/07/2019 10:00 AM TO 12:00 PM	WEDNESDAY	OBJECT ORIENTED ANALYSIS AND DESIGN [16MT20503]	POWER SYSTEM STABILITY AND CONTROL [16MT20703]	NANOELECTRONICS [16MT25703]
25/07/2019 10:00 AM TO 12:00 PM	THURSDAY	CLOUD COMPUTING [16MT12501]	RESTRUCTURED POWER SYSTEM [16MT20704]	PHYSICAL DESIGN AUTOMATION [16MT25704]
26/07/2019 10:00 AM TO 12:00 PM	FRIDAY	WEB TECHNOLOGIES [16MT22505]	STATIC AND DIGITAL PROTECTION OF POWER SYSTEM [16MT20705]	TESTING AND TESTABILITY [16MT25705]
27/07/2019 10:00 AM TO 12:00 PM	SATURDAY	INFORMATION SECURITY [16MT20505]	POWER QUALITY [16MT20708]	ASIC DESIGN [16MT13806]

1. Students have to bring their College Identity Cards to the examination hall; otherwise they will not be permitted to write the examination.
2. Examination will be on the Units prescribed in the Syllabus Book.
3. College buses will leave the college with in 15 minutes after Examination Time.


PRINCIPAL

Copy to: 1) The Director, SVEC.

- 2) The HODs of EEE, ECE, CSE : with a request to arrange to read out in the respective class rooms and circulation among the staff and to display on notice board.
- 3) The Examination Section.
- 4) The S.A.O, for information and necessary transport arrangement.