

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

IV B.Tech I Semester (SVEC-14) Supplementary Examinations, November 2019

TIME - TABLE

Date : 24/09/2019

Time : 02:00 PM To 05:00 PM

| EXAM DATE & TIME | WEEK DAY | BTECH-CE | BTECH-CSE | BTECH-CSSE | BTECH-ECE | BTECH-EEE | BTECH-EIE | BTECH-IT | BTECH-ME |
|-------------------------------------|----------|---|---|---|--|---|--|--|---|
| 19/11/2019 2:00 PM To 5:00 PM | TUESDAY | STEEL STRUCTURES [14BT70101] | SOFTWARE TESTING TECHNIQUES [14BT71204] | CRYPTOGRAPHY AND NETWORK SECURITY [14BT81202] KERNEL PROGRAMMING [14BT71503] | ELECTRONIC MEASUREMENTS AND INSTRUMENTATION [14BT70401] | POWER SEMICONDUCTOR DRIVES [14BT70201] | ANALYTICAL INSTRUMENTATION [14BT71001] | MOBILE APPLICATION DEVELOPMENT [14BT71201] | FINITE ELEMENT METHODS [14BT70301] |
| 21/11/2019 2:00 PM To 5:00 PM | THURSDAY | WATER RESOURCES ENGINEERING [14BT70102] | MOBILE COMPUTING [14BT70502] | SOFTWARE PROJECT MANAGEMENT [14BT71507] .NET TECHNOLOGIES [14BT81203] MOBILE COMPUTING [14BT70502] | EMBEDDED SYSTEMS [14BT70402] | POWER SYSTEM OPERATION AND CONTROL [14BT70202] | BIOMEDICAL INSTRUMENTATION [14BT71002] | MULTIMEDIA AND APPLICATIONS DEVELOPMENT [14BT71202] | MANUFACTURING SYSTEM DESIGN [14BT70302] |
| 23/11/2019 2:00 PM To 5:00 PM | SATURDAY | GEOSPATIAL TECHNOLOGIES [14BT70103] | COMPILER DESIGN [14BT70501] | OPTIMIZATION TECHNIQUES [14BT70331] | LIGHT WAVE COMMUNICATIONS [14BT70403] | POWER SYSTEM ANALYSIS [14BT70203] | LOGIC AND DISTRIBUTED CONTROL SYSTEMS [14BT71003] | COMPILER DESIGN [14BT70501] | METROLOGY & MEASUREMENTS [14BT70303] |
| 26/11/2019 2:00 PM To 5:00 PM | TUESDAY | TRANSPORTATION ENGINEERING- II [14BT70104] | DATA WAREHOUSING AND DATA MINING [14BT61201] | DATA WAREHOUSING AND DATA MINING [14BT61201] | COMPUTER NETWORKS [14BT51201] | DIGITAL SIGNAL PROCESSING [14BT60401] | ADVANCED CONTROL SYSTEMS [14BT60206] | COMPUTER GRAPHICS [14BT41501] | INDUSTRIAL AUTOMATION & ROBOTICS [14BT70304] |

| EXAM DATE & TIME | WEEK DAY | BTECH-CE | BTECH-CSE | BTECH-CSSE | BTECH-ECE | BTECH-EEE | BTECH-EIE | BTECH-IT | BTECH-ME |
|-------------------------------------|----------|--|--|---|--|---|--|--|---|
| 28/11/2019 2:00 PM To 5:00 PM | THURSDAY | ADVANCED FOUNDATION ENGINEERING [14BT70111] ENVIRONMENTAL IMPACT ASSESSMENT AND MANAGEMENT [14BT70114] | MACHINE LEARNING [14BT71205] EMBEDDED SYSTEM PROGRAMMING [14BT71501] SIMULATION AND MODELING [14BT71504] | EMBEDDED SYSTEM PROGRAMMING [14BT71501] | DIGITAL CMOS IC DESIGN [14BT70405] TELECOMMUNICATION SWITCHING SYSTEMS [14BT70406] OPERATING SYSTEMS [14BT51501] | REACTIVE POWER COMPENSATION AND MANAGEMENT [14BT70204] ADVANCED DATA STRUCTURES [14BT70531] PRINCIPLES OF COMMUNICATION [14BT60431] | VLSI DESIGN [14BT60403] COMPUTER NETWORKS [14BT51201] | SCRIPTING LANGUAGES [14BT71203] | REFRIGERATION AND AIR CONDITIONING [14BT70305] SUPPLY CHAIN MANAGEMENT [14BT70308] |
| 30/11/2019 2:00 PM To 5:00 PM | SATURDAY | ENVIRONMENTAL POLLUTION AND CONTROL [14BT70106] RURAL TECHNOLOGY [14BT70109] BANKING AND INSURANCE [14BT6HS01] | MULTIMEDIA AND APPLICATIONS DEVELOPMENT [14BT71202] HUMAN COMPUTER INTERACTION [14BT81503] | NETWORK PROGRAMMING [14BT71502] | ENVIRONMENTAL POLLUTION AND CONTROL [14BT70106] RURAL TECHNOLOGY [14BT70109] BANKING AND INSURANCE [14BT6HS01] | ENVIRONMENTAL POLLUTION AND CONTROL [14BT70106] RURAL TECHNOLOGY [14BT70109] BANKING AND INSURANCE [14BT6HS01] | ENVIRONMENTAL POLLUTION AND CONTROL [14BT70106] RURAL TECHNOLOGY [14BT70109] BANKING AND INSURANCE [14BT6HS01] | HUMAN COMPUTER INTERACTION [14BT81503] | POWER PLANT ENGINEERING [14BT70309] ADVANCED WELDING TECHNOLOGY [14BT70311] |


 P. C. C. 24/9/2019
 CHIEF CONTROLLER OF EXAMINATIONS


Copy to : The Director, SVEC.

The Vice-Principal, SVEC.

The Dean, Academics.

The HODs, of CE, ME, EEE, ECE, CSE, EIE, IT, CSSE and BS&H: with a request to arrange to display on notice Board.

The S.A.O, S.V.E.C; The Examination Section; The Manager, SVEC Canteen;

Estate Manager; Hostel Notice Boards.