

SREE VIDYANIKETHAN ENGINEERING COLLEGE (Autonomous)

Sree Sainath Nagar, Tirupati- 517 102

(Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu)

III B.Tech II Semester (SVEC-10) Supplementary Examinations December, 2019

EXAMINATION SCHEDULE

Date: 24-09-2019

Time: 10.00 AM To 01.00 PM

For 2012 & 2013 Regular and 2014 Lateral Entry Admitted Batches only

DATE /DAY	For 2012 & 2013 Regular and 2014 Lateral Entry Admitted Batches only									
	CIVIL	E.E.E	M.E	E.C.E	C.S.E	E.I.E	I.T	E.Con.E	C.S.S.E	Bio-Tech
04/12/2019 WEDNESDAY	ESTIMATION AND QUANTITY SURVEYING 10BT60101	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY 10BT4HS01	OPERATIONS RESEARCH 10BT60301	MANAGEMENT SCIENCE 10BT6HS01	THEORY OF COMPUTATION 10BT60501	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY 10BT4HS01	PRINCIPLES OF COMPILER DESIGN 10BT70501	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY 10BT4HS01	PRINCIPLES OF COMPILER DESIGN 10BT70501	MANAGEMENT SCIENCE 10BT6HS01
06/12/2019 FRIDAY	STEEL STRUCTURES - I 10BT60102	DIGITAL SIGNAL PROCESSING 10BT60401	METROLOGY & MEASUREMENTS 10BT60302	DIGITAL SIGNAL PROCESSING 10BT60401	UNIX PROGRAMMING 10BT60502	DIGITAL SIGNAL PROCESSING 10BT60401	UNIX PROGRAMMING 10BT60502	DIGITAL SIGNAL PROCESSING 10BT60401	PRINCIPLES OF PROGRAMMING LANGUAGES 10BT40503	BIOINFORMATICS 10BT62301
09/12/2019 MONDAY	WATER RESOURCES ENGINEERING 10BT60103	MICROPROCESSORS AND MICROCONTROLLERS 10BT60404	HEAT TRANSFER 10BT60303	MICROPROCESSORS AND MICROCONTROLLERS 10BT60404	DATA WAREHOUSING AND DATA MINING 10BT60503	MICROPROCESSORS AND MICROCONTROLLERS 10BT60404	DATA WAREHOUSING AND DATA MINING 10BT60503	MICROPROCESSORS AND MICROCONTROLLERS 10BT60404	COMPUTER GRAPHICS 10BT51202	IMMUNOLOGY 10BT62302
11/12/2019 WEDNESDAY	ENVIRONMENTAL ENGINEERING - I 10BT60104	LINEAR AND DIGITAL IC APPLICATIONS 10BT50422	CAD/CAM 10BT60304	DIGITAL COMMUNICATIONS 10BT60402	OBJECT ORIENTED ANALYSIS AND DESIGN 10BT61201	PRINCIPLES OF COMMUNICATIONS 10BT50423	OBJECT ORIENTED ANALYSIS AND DESIGN 10BT61201	OPTIMIZATION TECHNIQUES 10BT4EC01	WEB PROGRAMMING 10BT71201	ENZYME ENGINEERING 10BT62303
13/12/2019 FRIDAY	TRANSPORTATION ENGINEERING 10BT60105	UTILIZATION OF ELECTRICAL ENERGY 10BT60201	DESIGN OF MACHINE ELEMENTS-II 10BT60305	MICROWAVE ENGINEERING 10BT60403	DISTRIBUTED COMPUTING 10BT60504	OPTOELECTRONIC AND LASER INSTRUMENTATION 10BT61001	COMPUTER NETWORKS 10BT61202	DIGITAL CONTROL SYSTEMS 10BT61301	COMPUTER NETWORKS 10BT61202	APPLIED TRANSPORT PHENOMENA 10BT62304
17/12/2019 TUESDAY	FOUNDATION ENGINEERING 10BT60106	POWER SEMICONDUCTOR DRIVES 10BT60202	AUTOMOBILE ENGINEERING 10BT60306	VLSI DESIGN 10BT60405	CRYPTOGRAPHY AND NETWORK SECURITY 10BT71204	BIOMEDICAL INSTRUMENTATION 10BT61002	DESIGN AND ANALYSIS OF ALGORITHMS 10BT50501	ROBOTICS AND AUTOMATION 10BT61302	MODELING AND SIMULATION 10BT61501	BIOPROCESS DYNAMICS AND CONTROL 10BT62305

P. Chelvan
CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC.

The Vice-Principal, SVEC. The Dean, Academics.

The HODs, of CE, EEE, ME, ECE, CSE, EIE, IT, CSSE & BS&H: with a request to arrange to display on notice Board. The Examination Section.

The S.A.O, SVEC. The Manager, SVEC Canteen; Estate Manager; Hostel Notice Boards.