

# Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

M.Tech I Semester (SVEC-19) Regular Examinations, February 2020

## TIME - TABLE

Date : 23/12/2019

Time : 10:00 AM TO 01:00 PM

EXAM DATE & TIME	WEEK DAY	MTECH-CS	MTECH-EPS	MTECH-PED	MTECH-VLSI
10/02/2020 10:00AM TO 1:00PM	MONDAY	ADVANCED ALGORITHMS [19MT10501]	HIGH VOLTAGE ENGINEERING [19MT10701]	DYNAMICS OF ELECTRICAL MACHINES [19MT18301]	ANALOG CMOS VLSI DESIGN [19MT15701]
12/02/2020 10:00AM TO 1:00PM	WEDNESDAY	ADVANCED DATA STRUCTURES [19MT10502]	POWER ELECTRONICS FOR POWER SYSTEMS [19MT10702]	POWER ELECTRONIC CONVERTERS [19MT18302]	DEVICE MODELING [19MT15702]
14/02/2020 10:00AM TO 1:00PM	FRIDAY	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE [19MT10503]	POWER SYSTEM SECURITY AND STATE ESTIMATION [19MT10703]	POWER SEMICONDUCTOR DEVICES AND MODELING [19MT18303]	DIGITAL CMOS VLSI DESIGN [19MT15703]
17/02/2020 10:00AM TO 1:00PM	MONDAY	DATA WAREHOUSING AND DATA MINING [19MT10505]	INTELLIGENT CONTROLLERS [19MT18305]	INTELLIGENT CONTROLLERS [19MT18305]	IC FABRICATION [19MT15705]
19/02/2020 10:00AM TO 1:00PM	WEDNESDAY	CLOUD COMPUTING [19MT16303]	POWER QUALITY [19MT10706]	POWER QUALITY [19MT10706]	MIXED SIGNAL DESIGN [19MT15709]
22/02/2020 10:00AM TO 1:00PM	SATURDAY	RESEARCH METHODOLOGY AND IPR [19MT10708]	RESEARCH METHODOLOGY AND IPR [19MT10708]	RESEARCH METHODOLOGY AND IPR [19MT10708]	RESEARCH METHODOLOGY AND IPR [19MT10708]

  
CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC

The Vice-Principal, SVEC

The Dean, Academics, SVEC

The HODs, of EEE, ECE & CSE : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.

The S.A.O, S.V.E.C

The Examination Section

The Manager, SVEC Canteen..

The Estate Manager, SVEC

Hostel Notice Boards