

Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh
M.Tech I Semester (SVEC-16) Supplementary Examinations, February 2020

TIME - TABLE

Date : 23/12/2019

Time : 10:00 AM TO 01:00 PM

EXAM DATE & TIME	WEEK DAY	MTECH-CMS	MTECH-CNIS	MTECH-CS	MTECH-DECS	MTECH-EPS	MTECH-PED	MTECH-SE	MTECH-VLSI
10/02/2020 10:00AM TO 1:00PM	MONDAY	ADVANCED DIGITAL SIGNAL PROCESSING [16MT15706]	ADVANCED COMPUTER NETWORKS [16MT10501]	ADVANCED COMPUTER NETWORKS [16MT10501]	COMPUTER NETWORKS [16MT13801]	ADVANCED CONTROL SYSTEMS [16MT10701]	APPLIED MATHEMATICS [16MT18501]	CLOUD COMPUTING [16MT12501]	ANALOG IC DESIGN [16MT15701]
12/02/2020 10:00AM TO 1:00PM	WEDNESDAY	DIGITAL COMMUNICATION TECHNIQUES [16MT13802]	INFORMATION SECURITY [16MT20505]	ADVANCED DATABASE MANAGEMENT SYSTEMS [16MT10502]	DIGITAL COMMUNICATION TECHNIQUES [16MT13802]	HIGH VOLTAGE ENGINEERING [16MT10702]	ADVANCED POWER SEMICONDUCTOR DEVICES [16MT18301]	SOFTWARE DEVELOPMENT METHODOLOGIES [16MT12503]	COMPUTATIONAL METHODS IN MICROELECTRONICS [16MT15702]
14/02/2020 10:00AM TO 1:00PM	FRIDAY	INFORMATION THEORY AND CODING TECHNIQUES [16MT23803]	MOBILE COMPUTING [16MT20506]	ADVANCED OPERATING SYSTEMS [16MT10503]	DIGITAL SYSTEM DESIGN AND TESTING [16MT13803]	POWER ELECTRONIC CONVERTERS [16MT10703]	ANALYSIS OF INVERTERS [16MT18302]	SOFTWARE MEASUREMENT AND METRICS [16MT12504]	DEVICE MODELING [16MT15703]
17/02/2020 10:00AM TO 1:00PM	MONDAY	OPTICAL COMMUNICATIONS AND NETWORKS [16MT23807]	DATA WAREHOUSING AND DATA MINING [16MT10504]	DATA WAREHOUSING AND DATA MINING [16MT10504]	IMAGE & VIDEO PROCESSING [16MT13804]	POWER SYSTEM SECURITY AND STATE ESTIMATION [16MT10704]	ANALYSIS OF POWER CONVERTERS [16MT18303]	SOFTWARE REQUIREMENTS AND ESTIMATION [16MT12505]	DIGITAL IC DESIGN [16MT15704]
19/02/2020 10:00AM TO 1:00PM	WEDNESDAY	RF CIRCUIT DESIGN [16MT16101]	WEB TECHNOLOGIES [16MT22505]	DATA STRUCTURES AND ALGORITHMS [16MT12502]	MODERN DIGITAL SIGNAL PROCESSING [16MT13805]	REACTIVE POWER COMPENSATION AND MANAGEMENT [16MT10705]	MODELLING OF ELECTRICAL MACHINES [16MT18304]	DATA STRUCTURES AND ALGORITHMS [16MT12502]	IC FABRICATION [16MT15705]
22/02/2020 10:00AM TO 1:00PM	SATURDAY	DIGITAL SATELLITE COMMUNICATIONS [16MT16102]	INTERNET OF THINGS [16MT10507]	SOFTWARE TESTING TECHNIQUES [16MT22504]	ASIC DESIGN [16MT13806]	MICROCONTROLLER S AND APPLICATIONS [16MT10707] POWER SYSTEM RELIABILITY [16MT10708]	MICROCONTROLLER S AND APPLICATIONS [16MT10707]	USER INTERFACE DESIGN [16MT12509]	REAL TIME SYSTEMS [16MT23808] FPGA ARCHITECTURES AND APPLICATIONS (16MT15707)


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, SVEC

The Vice-Principal, SVEC

The Dean, Academics, SVEC

The HODs, of EEE, ECE, CSE & IT: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.

The S.A.O, S.V.E.C

The Examination Section

The Manager, SVEC Canteen..

The Estate Manager, SVEC

Hostel Notice Boards