SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

IV B.Tech II Semester (SVEC-16) (RA) Regular Examinations, September 2020 <u>TIME - TABLE</u>

Date: 24/08/2020

Time: 02:00 PM To 05:00 PM

EXAM DATE & TIME	WEEK DAY	BTECH-CE	BTECH-CSE	BTECH-CSSE	BTECH-ECE	BTECH-EEE	BTECH-EIE	BTECH-IT	BTECH-ME
07/09/2020 2:00 PM		ENGINEERING	BIG DATA ANALYTICS	SOFTWARE PROJECT	LINEAR IC APPLICATIONS [16BT40404]	COMPUTER ARCHITECTURE	INDUSTRIAL	LINUX	PRODUCT DESIGN
To 5:00 PM	MONDAY	HYDROLOGY [16BT40103]	[16BT70501]	MANAGEMENT [16BT61503]	COMPUTER NETWORKS [16BT50501]	AND ORGANIZATION [16BT41041]	INSTRUMENTATION – I [16BT41001]	PROGRAMMING [16BT50502]	[16BT70310]
09/09/2020 2:00 PM To 5:00 PM	WEDNESDAY	WATER SUPPLY ENGINEERING [16BT40105]	HUMAN COMPUTER INTERACTION [16BT70505]	MOBILE COMPUTING [16BT71204]	DIGITAL IC APPLICATIONS [16BT40402]	SPECIAL ELECTRICAL MACHINES [16BT60210]	LINEAR AND DIGITAL ICS [16BT41002]	INFORMATION RETRIEVAL SYSTEMS [16BT71203]	POWER PLANT ENGINEERING [16BT70312]

CHIEF CONTROLLER OF EXAMINATIONS

Copy to: The Vice-Principal, SVEC.

The Dean, Academics, SVEC.

The HODs, of CE, ME, EEE, ECE, CSE, EIE, IT & CSSE: with a request to arrange to inform the students and staff members.

The S.A.O, S.V.E.C
The Examination Section
Hostel Notice Boards.