Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh II B. Tech II Semester (SVEC-16) Supplementary Examinations, October 2020

REVISED TIME - TABLE

Date: 15/09/2020

Time: 10:00 AM To 01:00 PM

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EXAM DATE & TIME	WEEK DAY	BTECH-CE	BTECH-CSE	BTECH-CSSE	BTECH-ECE	BTECH-EEE	BTECH-EIE	BTECH-IT	BTECH-ME
13/10/2020 10:00 AM To	TUESDAY	ENVIRONMENTAL STUDIES [16BT3HS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	ANALOG COMMUNICATIONS [16BT40401]	ELECTRICAL MEASUREMENTS [16BT40201]	CONTROL SYSTEMS	ENVIRONMENTAL STUDIES [16BT3HS01]	ENVIRONMENTAL STUDIES [16BT3HS01]
1:00 PM 15/10/2020 10:00 AM To 1:00 PM	THURSDAY	CONCRETE TECHNOLOGY	COMPUTER GRAPHICS	THEORY OF COMPUTATION	DIGITAL IC APPLICATIONS	GENERATION OF ELECTRIC POWER	ELECTRONIC CIRCUIT ANALYSIS AND DESIGN	THEORY OF COMPUTATION	DESIGN OF MACHINE ELEMENTS-I
		[16BT40101]	[16BT40501]	[16BT41204]	[16BT40402]	[16BT40202]	[16BT30401]	[16BT41204]	[16BT40301]
17/10/2020 10:00 AM To 1:00 PM	SATURDAY	ENGINEERING GEOLOGY	DATABASE MANAGEMENT SYSTEMS	DATABASE MANAGEMENT SYSTEMS	ELECTROMAGNETIC THEORY AND TRANSMISSION LINES	TRANSFORMERS AND INDUCTION MACHINES	INDUSTRIAL INSTRUMENTATION-I	DATABASE MANAGEMENT SYSTEMS	DYNAMICS OF MACHINERY
		[16BT40102]	[16BT40502]	[16BT40502]	[16BT40403]	[16BT40203]	[16BT41001]	[16BT40502]	[16BT40302]
20/10/2020 10:00 AM To 1:00 PM	TUESDAY	ENGINEERING HYDROLOGY	DESIGN AND ANALYSIS OF ALGORITHMS	DESIGN AND ANALYSIS OF ALGORITHMS	LINEAR IC APPLICATIONS	LINEAR AND DIGITAL ICS	LINEAR AND DIGITAL ICS	DESIGN AND ANALYSIS OF ALGORITHMS	FLUID MECHANICS
		[16BT40103]	[16BT41201]	[16BT41201]	[16BT40404]	[16BT41002]	[16BT41002]	[16BT41201]	[16BT40303]
22/10/2020 10:00 AM To 1:00 PM	THURSDAY	STRUCTURAL ANALYSIS-I	JAVA PROGRAMMING	OBJECT ORIENTED ANALYSIS AND DESIGN	PROBABILITY AND STOCHASTIC PROCESS	SWITCHING THEORY AND LOGIC DESIGN	SIGNALS AND SYSTEMS	JAVA PROGRAMMING	MACHINE TOOLS AND MODERN MACHINING PROCESS
		[16BT40104]	[16BT41202]	[16BT51202]	[16BT40405]	[16BT30403]	[16BT30402]	[16BT41202]	[16BT40304]
27/10/2020 10:00 AM To 1:00 PM	TUESDAY	WATER SUPPLY ENGINEERING	SOFTWARE ENGINEERING	SOFTWARE ENGINEERING	PULSE AND DIGITAL CIRCUITS	COMPUTER ARCHITECTURE AND ORGANIZATION	PULSE AND DIGITAL CIRCUITS	SOFTWARE ENGINEERING	THERMAL ENGINEERING -I
		[16BT40105]	[16BT41203]	[16BT41203]	[16BT40406]	[16BT41041]	[16BT40406]	[16BT41203]	[16BT40305]

CHIEF CONTROLLER OF EXAMINATIONS

Copy to: The Vice-Principal, SVEC.

The Dean, Academics.

The HODs, of CE, ME, EEE, ECE, CSE, EIE, IT, CSSE and BS&H: with a request to arrange to display on notice Board.

The S.A.O, S.V.E.C; The Examination Section; The Manager, SVEC Canteen;

Estate Manager; Hostel Notice Boards.