

Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

II B.Tech I Semester (SVEC-16) Supplementary Examinations, September 2020

REVISED TIME - TABLE

Date : 15/09/2020

Time : 10:00 AM To 01:00 PM

| EXAM DATE & TIME | WEEK DAY | BTECH-CE | BTECH-CSE | BTECH-CSSE | BTECH-ECE | BTECH-EEE | BTECH-EIE | BTECH-IT | BTECH-ME |
|--------------------------------------|-----------|---|--|--|---|---|---|--|--|
| 28/09/2020 10:00 AM To 1:00 PM | MONDAY | PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01] | PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01] | PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01] | ENVIRONMENTAL STUDIES [16BT3HS01] | ENVIRONMENTAL STUDIES [16BT3HS01] | ENVIRONMENTAL STUDIES [16BT3HS01] | PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01] | PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01] |
| 30/09/2020 10:00 AM To 1:00 PM | WEDNESDAY | MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY [16BT3HS02] | COMPUTER ORGANIZATION [16BT30501] | COMPUTER ORGANIZATION [16BT30501] | SPECIAL FUNCTIONS AND COMPLEX ANALYSIS [16BT3BS02] | SPECIAL FUNCTIONS AND COMPLEX ANALYSIS [16BT3BS02] | SPECIAL FUNCTIONS AND COMPLEX ANALYSIS [16BT3BS02] | COMPUTER ORGANIZATION [16BT30501] | ENGINEERING METALLURGY [16BT30301] |
| 03/10/2020 10:00 AM To 1:00 PM | SATURDAY | CONSTRUCTION PLANNING AND PROJECT MANAGEMENT [16BT30101] | DATA STRUCTURES [16BT30502] | DATA STRUCTURES [16BT30502] | ELECTRONIC CIRCUIT ANALYSIS & DESIGN [16BT30401] | DC MACHINES [16BT30201] | ELECTRICAL AND ELECTRONIC MEASUREMENTS [16BT31001] | DATA STRUCTURES [16BT30502] | KINEMATICS OF MACHINERY [16BT30302] |
| 06/10/2020 10:00 AM To 1:00 PM | TUESDAY | FLUID MECHANICS AND HYDRAULIC MACHINERY [16BT30102] | DISCRETE MATHEMATICAL STRUCTURES [16BT31201] | DISCRETE MATHEMATICAL STRUCTURES [16BT31201] | SIGNALS AND SYSTEMS [16BT30402] | ELECTROMAGNETIC FIELDS [16BT30202] | SENSORS AND TRANSDUCERS [16BT31002] | DISCRETE MATHEMATICAL STRUCTURES [16BT31201] | MANUFACTURING TECHNOLOGY [16BT30303] |
| 08/10/2020 10:00 AM To 1:00 PM | THURSDAY | MECHANICS OF SOLIDS [16BT30103] | PYTHON PROGRAMMING [16BT30503] | JAVA PROGRAMMING [16BT41202] | SWITCHING THEORY AND LOGIC DESIGN [16BT30403] | SIGNALS, SYSTEMS AND NETWORKS [16BT30203] | SWITCHING THEORY AND LOGIC DESIGN [16BT30403] | LINUX PROGRAMMING [16BT50502] | STRENGTH OF MATERIALS [16BT30304] |
| 10/10/2020 10:00 AM To 1:00 PM | SATURDAY | SURVEYING [16BT30104] | OPERATING SYSTEMS [16BT31501] | OPERATING SYSTEMS [16BT31501] | ELECTRICAL TECHNOLOGY [16BT30241] | ANALOG ELECTRONIC CIRCUITS [16BT30441] | ELECTRICAL TECHNOLOGY [16BT30241] | OPERATING SYSTEMS [16BT31501] | THERMODYNAMICS [16BT30305] |


CHIEF CONTROLLER OF EXAMINATIONS

Copy to: The Vice-Principal, SVEC.

The Dean, Academics.

The HODs, of CE, ME, EEE, ECE, CSE, EIE, IT, CSSE and BS&H: with a request to arrange to display on notice Board.

The S.A.O, S.V.E.C; The Examination Section; The Manager, SVEC Canteen;

Estate Manager; Hostel Notice Boards.