

Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

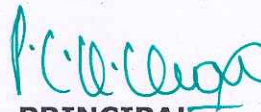

I B.Tech II Semester (SVEC-19) Mid- I Examinations, November - 2020

TIME - TABLE

Date : 02/11/2020

EXAM DATE & TIME	WEEK DAY	BTECH-CE	BTECH-CSE	BTECH-CSSE	BTECH-ECE	BTECH-EEE	BTECH-EIE	BTECH-IT	BTECH-ME
16/11/2020 09:30 AM To 11:30 AM	MONDAY	TRANSFORMATION TECHNIQUES AND LINEAR ALGEBRA [19BT2BS01]	TRANSFORMATION TECHNIQUES AND LINEAR ALGEBRA [19BT2BS01]	TRANSFORMATION TECHNIQUES AND LINEAR ALGEBRA [19BT2BS01]	TRANSFORMATION TECHNIQUES AND LINEAR ALGEBRA [19BT2BS01]	TRANSFORMATION TECHNIQUES AND LINEAR ALGEBRA [19BT2BS01]	TRANSFORMATION TECHNIQUES AND LINEAR ALGEBRA [19BT2BS01]	TRANSFORMATION TECHNIQUES AND LINEAR ALGEBRA [19BT2BS01]	TRANSFORMATION TECHNIQUES AND LINEAR ALGEBRA [19BT2BS01]
17/11/2020 09:30 AM To 11:30 AM	TUESDAY	BIOLOGY FOR ENGINEERS [19BT1BS02]	BIOLOGY FOR ENGINEERS [19BT1BS02]	BIOLOGY FOR ENGINEERS [19BT1BS02]	ENGINEERING CHEMISTRY [19BT1BS04]	ENGINEERING CHEMISTRY [19BT1BS04]	ENGINEERING CHEMISTRY [19BT1BS04]	BIOLOGY FOR ENGINEERS [19BT1BS02]	BIOLOGY FOR ENGINEERS [19BT1BS02]
18/11/2020 09:30 AM To 11:30 AM	WEDNESDAY	APPLIED PHYSICS [19BT2BS02]	ENGINEERING PHYSICS [19BT1BS03]	ENGINEERING PHYSICS [19BT1BS03]	COMMUNICATIVE ENGLISH [19BT1HS01]	COMMUNICATIVE ENGLISH [19BT1HS01]	COMMUNICATIVE ENGLISH [19BT1HS01]	ENGINEERING PHYSICS [19BT1BS03]	APPLIED PHYSICS [19BT2BS02]
19/11/2020 09:30 AM To 11:30 AM	THURSDAY	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING [19BT10201]	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING [19BT10201]	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING [19BT10201]	PROGRAMMING FOR PROBLEM SOLVING [19BT10501]	PROGRAMMING FOR PROBLEM SOLVING [19BT10501]	PROGRAMMING FOR PROBLEM SOLVING [19BT10501]	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING [19BT10201]	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING [19BT10201]
20/11/2020 09:30 AM To 11:30 AM	FRIDAY	CIVIL ENGINEERING MATERIALS AND CONCRETE TECHNOLOGY [19BT20101]	DIGITAL LOGIC DESIGN [19BT20501]	DIGITAL LOGIC DESIGN [19BT20501]	NETWORK ANALYSIS [19BT20241]	ELECTRIC CIRCUITS [19BT20201]	NETWORK ANALYSIS [19BT20241]	DIGITAL LOGIC DESIGN [19BT20501]	BASIC ENGINEERING MECHANICS [19BT20301]
21/11/2020 09:30 AM To 11:30 AM	SATURDAY	ENGINEERING MECHANICS [19BT20102]	OBJECT ORIENTED PROGRAMMING THROUGH JAVA [19BT21501]	OBJECT ORIENTED PROGRAMMING THROUGH JAVA [19BT21501]	-	-	-	OBJECT ORIENTED PROGRAMMING THROUGH JAVA [19BT21501]	MATERIAL SCIENCE AND ENGINEERING [19BT20302]

1. Students have to bring their College Identity Cards to the examination hall; otherwise they will not be permitted to write the examination.
2. Examination will be on the Units prescribed in the Syllabus Book.
3. College buses will leave the college with in 15 minutes after Examination Time.


PRINCIPAL


- Copy to:
- 1) The Vice- Principal, SVEC.
 - 2) The Dean, Academics.
 - 3) The HODs of CE, EEE, ME, ECE, CSE, EIE, IT, CSSE & BS&H: with a request to arrange to read out in the respective class rooms and circulation among the staff and to display on notice board.
 - 4) The Examination Section.
 - 5) The S.A.O, for information.
 - 6) The Office, ISA.