

# Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh  
M.Tech I Semester (SVEC-19) Supplementary Examinations, December 2020


## TIME - TABLE

Date : 16/11/2020

Time : 02:00 PM TO 05:00 PM

EXAM DATE & TIME	WEEK DAY	MTECH-CS	MTECH-EPS	MTECH-PED	MTECH-VLSI
11/12/2020 02:00 PM TO 05:00PM	FRIDAY	ADVANCED ALGORITHMS [19MT10501]	HIGH VOLTAGE ENGINEERING [19MT10701]	DYNAMICS OF ELECTRICAL MACHINES [19MT18301]	ANALOG CMOS VLSI DESIGN [19MT15701]
14/12/2020 02:00 PM TO 05:00PM	MONDAY	ADVANCED DATA STRUCTURES [19MT10502]	POWER ELECTRONICS FOR POWER SYSTEMS [19MT10702]	POWER ELECTRONIC CONVERTERS [19MT18302]	DEVICE MODELING [19MT15702]
16/12/2020 02:00 PM TO 05:00PM	WEDNESDAY	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE [19MT10503]	POWER SYSTEM SECURITY AND STATE ESTIMATION [19MT10703]	POWER SEMICONDUCTOR DEVICES AND MODELING [19MT18303]	DIGITAL CMOS VLSI DESIGN [19MT15703]
18/12/2020 02:00 PM TO 05:00PM	FRIDAY	DATA WAREHOUSING AND DATA MINING [19MT10505]	INTELLIGENT CONTROLLERS [19MT18305]	INTELLIGENT CONTROLLERS [19MT18305]	IC FABRICATION [19MT15705]
21/12/2020 02:00 PM TO 05:00PM	MONDAY	CLOUD COMPUTING [19MT16303]	POWER QUALITY [19MT10706]	POWER QUALITY [19MT10706]	MIXED SIGNAL DESIGN [19MT15709]
23/12/2020 02:00 PM TO 05:00PM	WEDNESDAY	RESEARCH METHODOLOGY AND IPR [19MT10708]	RESEARCH METHODOLOGY AND IPR [19MT10708]	RESEARCH METHODOLOGY AND IPR [19MT10708]	RESEARCH METHODOLOGY AND IPR [19MT10708]

Copy to : The Vice-Principal, SVEC  
The Dean, Academics, SVEC  
The HODs, of EEE, ECE & CSE : with a request to arrange to read over in the respective class rooms and circulate among the staff members  
The S.A.O, S.V.E.C  
The Examination Section  
The Manager, SVEC Canteen..  
The Estate Manager, SVEC  
Hostel Notice Boards

  
CHIEF CONTROLLER OF EXAMINATIONS