

Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh
I B.Tech II Semester (SVEC-16) Supplementary Examinations, March 2021

TIME - TABLE

Date : 11/01/2021

Time : 02:00 PM To 05:00 PM

| EXAM DATE & TIME | WEEK DAY | BTECH-CE | BTECH-CSE | BTECH-CSSE | BTECH-ECE | BTECH-EEE | BTECH-EIE | BTECH-IT | BTECH-ME |
|---------------------------------------|-----------|---|---|---|---|---|---|---|---|
| 12/03/2021 02:00 PM To 05:00 PM | FRIDAY | ENGINEERING PHYSICS [16BT1BS02] | ENGINEERING PHYSICS [16BT1BS02] | ENGINEERING PHYSICS [16BT1BS02] | TECHNICAL ENGLISH [16BT1HS01] | TECHNICAL ENGLISH [16BT1HS01] | TECHNICAL ENGLISH [16BT1HS01] | ENGINEERING PHYSICS [16BT1BS02] | ENGINEERING PHYSICS [16BT1BS02] |
| 16/03/2021 02:00 PM To 05:00 PM | TUESDAY | TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01] | TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01] | TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01] | TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01] | TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01] | TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01] | TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01] | TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01] |
| 23/03/2021 02:00 PM To 05:00 PM | TUESDAY | BUILDING MATERIAL AND CONSTRUCTION TECHNOLOGY [16BT20101] | BASIC ELECTRONIC DEVICES AND CIRCUITS [16BT20441] | BASIC ELECTRONIC DEVICES AND CIRCUITS [16BT20441] | ELECTRONIC DEVICES AND CIRCUITS [16BT20401] | ELECTRONIC DEVICES AND CIRCUITS [16BT20401] | ELECTRONIC DEVICES AND CIRCUITS [16BT20401] | BASIC ELECTRONIC DEVICES AND CIRCUITS [16BT20441] | ENGINEERING MATERIALS [16BT20301] |
| 26/03/2021 02:00 PM To 05:00 PM | FRIDAY | ENGINEERING MECHANICS [16BT20102] | OBJECT ORIENTED PROGRAMMING THROUGH C++ [16BT21201] | OBJECT ORIENTED PROGRAMMING THROUGH C++ [16BT21201] | ENGINEERING CHEMISTRY [16BT1BS01] | ENGINEERING CHEMISTRY [16BT1BS01] | ENGINEERING CHEMISTRY [16BT1BS01] | OBJECT ORIENTED PROGRAMMING THROUGH C++ [16BT21201] | ENGINEERING MECHANICS [16BT20102] |
| 31/03/2021 02:00 PM To 05:00 PM | WEDNESDAY | BASIC ELECTRICAL AND ELECTRONICS ENGINEERING [16BT20241] | DIGITAL LOGIC DESIGN [16BT21501] | DIGITAL LOGIC DESIGN [16BT21501] | FOUNDATIONS OF DATA STRUCTURES [16BT20541] | FOUNDATIONS OF DATA STRUCTURES [16BT20541] | FOUNDATIONS OF DATA STRUCTURES [16BT20541] | DIGITAL LOGIC DESIGN [16BT21501] | BASIC ELECTRICAL AND ELECTRONICS ENGINEERING [16BT20241] |


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Vice-Principal, SVEC.
The Dean, Academics.

The HODs, of CE, ME, EEE, ECE, CSE, EIE, IT, CSSE and BS&H: with a request to arrange to display on notice Board.

The S.A.O, SVEC for information and necessary transport arrangement.

The Examination Section; The Manager, SVEC Canteen;