

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

IV B.Tech II Semester (SVEC-16) Regular Examinations, June 2021

(For Re-admitted students only)

TIME - TABLE

Date : 11/06/2021

Time : 02:00 PM To 05:00 PM

EXAM DATE & TIME	WEEK DAY	BTECH-CE	BTECH-CSE	BTECH-CSSE	BTECH-ECE	BTECH-EEE	BTECH-EIE	BTECH-IT	BTECH-ME
21/06/2021 02:00 PM TO 05:00 PM	MONDAY	--	--	SOFTWARE PROJECT MANAGEMENT (16BT61503)	--	--	SWITCHING THEORY AND LOGIC DESIGN (16BT30403)	--	--
23/06/2021 02:00 PM TO 05:00 PM	WEDNESDAY	--	--	MOBILE COMPUTING (16BT71204)	--	--	ELECTRICAL TECHNOLOGY (16BT30241)	--	--


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, Academics, SVEC.

The Vice-Principal, SVEC.

The Dean, Academics.

The Dean, Placements.

The HODs, of EIE & CSSE: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.

The Examination Section.

The S.A.O, SVEC for information and necessary transport arrangement.

Hostel Notice Boards.

The Office, ISA.

ACE-2
for n.a
11/6/2021