

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

IV B.Tech II Semester (SVEC-16) Mid-II Theory Examinations, June - 2021

(For Re-admitted students only)

TIME - TABLE

Date : 11/06/2021

Time : 09:00 AM To 11:00 AM

EXAM DATE & TIME	WEEK DAY	BTECH-CE	BTECH-CSE	BTECH-CSSE	BTECH-ECE	BTECH-EEE	BTECH-EIE	BTECH-IT	BTECH-ME
21/06/2021 09:00 AM TO 11:00 AM	MONDAY	--	--	SOFTWARE PROJECT MANAGEMENT (16BT61503)	--	--	SWITCHING THEORY AND LOGIC DESIGN (16BT30403)	--	--
23/06/2021 09:00 AM TO 11:00 AM	WEDNESDAY	--	--	MOBILE COMPUTING (16BT71204)	--	--	ELECTRICAL TECHNOLOGY (16BT30241)	--	--

1. Students have to bring their College Identity Cards to the examination hall; otherwise they will not be permitted to write the examination.

2. Examination will be on the Units prescribed in the Syllabus Book.


PRINCIPAL


- Copy to: 1) The Director, Academics.
2) The Vice-Principal, SVEC.
3) The Dean, Academics.
4) The Dean, Placements.
5) The HODs of EIE & CSSE: with a request to arrange to read out in the respective class rooms and circulation among the staff members and to display on notice board.
6) The Examination Section.
7) The S.A.O, for information.
8) The Office, ISA.
9) Notice Boards.

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11/6/2021