Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati,Chittoor,Andhra Pradesh

M.Tech I Semester (SVEC 19) Mid-II Examinations, August 2021

TIME - TABLE

Date : 14.07.2021

Time : 02:30 PM TO 04:30 PM

EXAM DATE & TIME	WEEK DAY	MTECH-CS	MTECH-VLSI
02/08/2021 2:30 PM TO 4:30 PM	MONDAY	Advanced Algorithms 19MT10501	Analog CMOS VLSI Design 19MT15701
03/08/2021 2:30 PM TO 4:30 PM	TUESDAY	Advanced Data Structures 19MT10502	Device Modeling 19MT15702
04/08/2021 2:30 PM TO 4:30 PM	WEDNESDAY	Mathematical Foundations of Computer Science 19MT10503	Digital CMOS VLSI Design 19MT15703
05/08/2021 2:30 PM TO 4:30 PM	THURSDAY	Wireless Sensor Networks 19MT26302	IC Fabrication 19MT15705
06/08/2021 2:30 PM TO 4:30 PM	FRIDAY	Cloud Computing 19MT16303	FPGA Architectures 19MT15707
07/08/2021 2:30 PM TO 4:30 PM	SATURDAY	Research Methodology and IPR 19MT10708	Research Methodology and IPR 19MT10708

1. Students have to bring their College Identity Cards to the examination hall; otherwise they will not be permitted to write the examination.
2. Examination will be on the Units prescribed in the Syllabus Book.
3. College buses will leave the college with in 15 minutes after Examination Time.

14/7/2021

PRINCIPAL

Copy to: 1) The Director, Academics.
2) The Vice-Principal, SVEC.
3) The Dean Academics, SVEC.
4) The Dean Placements &
5) The HOD of ECE & CSE: with a request to arrange to read out in the respective class rooms and circulation among the staff and to display on notice board.
6) The Examination Section.
7) The S.A.O, for information and necessary transport
8) Hostel Notice Boards
9) The office,ISA