

# Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

M.Tech I Semester (SVEC 19) Regular Examinations, August 2021

## TIME - TABLE

Date : 14.07.2021

Time : 02:00 PM TO 05:00 PM

EXAM DATE & TIME	WEEK DAY	MTECH-CS	MTECH-VLSI
09/08/2021 2:00 PM TO 5:00 PM	MONDAY	Advanced Algorithms 19MT10501	Analog CMOS VLSI Design 19MT15701
11/08/2021 2:00 PM TO 5:00 PM	WEDNESDAY	Advanced Data Structures 19MT10502	Device Modeling 19MT15702
13/08/2021 2:00 PM TO 5:00 PM	FRIDAY	Mathematical Foundations of Computer Science 19MT10503	Digital CMOS VLSI Design 19MT15703
16/08/2021 2:00 PM TO 5:00 PM	MONDAY	Wireless Sensor Networks 19MT26302	IC Fabrication 19MT15705
18/08/2021 2:00 PM TO 5:00 PM	WEDNESDAY	Cloud Computing 19MT16303	FPGA Architectures 19MT15707
21/08/2021 2:00 PM TO 5:00 PM	SATURDAY	Research Methodology and IPR 19MT10708	Research Methodology and IPR 19MT10708

*Signature* 14/7/2021  
**CHIEF CONTROLLER OF EXAMINATIONS**  
*Yamini*

Copy to : The Director, Academics, SVEC.

The Vice-Principal, SVEC.

The Dean, Academics,

The Dean, Placements,

The HOD of ECE & CSE: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.

The Examination Section.

The S.A.O, SVEC.

Hostel Notice Boards.

The Office, ISA.