

Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh
M.Tech I Semester (SVEC-19) Supplementary Examinations, August 2021

TIME - TABLE

Date : 14.07.2021

Time : 02:00 PM TO 05:00 PM

EXAM DATE & TIME	WEEK DAY	MTECH-CS	MTECH-EPS	MTECH-PED	MTECH-VLSI
09/08/2021 02:00 PM TO 5:00 PM	MONDAY	ADVANCED ALGORITHMS [19MT10501]	HIGH VOLTAGE ENGINEERING [19MT10701]	DYNAMICS OF ELECTRICAL MACHINES [19MT18301]	ANALOG CMOS VLSI DESIGN [19MT15701]
11/08/2021 02:00 PM TO 5:00 PM	WEDNESDAY	ADVANCED DATA STRUCTURES [19MT10502]	POWER ELECTRONICS FOR POWER SYSTEMS [19MT10702]	POWER ELECTRONIC CONVERTERS [19MT18302]	DEVICE MODELING [19MT15702]
13/08/2021 02:00 PM TO 5:00 PM	FRIDAY	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE [19MT10503]	POWER SYSTEM SECURITY AND STATE ESTIMATION [19MT10703]	POWER SEMICONDUCTOR DEVICES AND MODELING [19MT18303]	DIGITAL CMOS VLSI DESIGN [19MT15703]
16/08/2021 02:00 PM TO 5:00 PM	MONDAY	DATA WAREHOUSING AND DATA MINING [19MT10505]	INTELLIGENT CONTROLLERS [19MT18305]	INTELLIGENT CONTROLLERS [19MT18305]	IC FABRICATION [19MT15705]
18/08/2021 02:00 PM TO 5:00 PM	WEDNESDAY	CLOUD COMPUTING [19MT16303]	POWER QUALITY [19MT10706]	POWER QUALITY [19MT10706]	MIXED SIGNAL DESIGN [19MT15709]
21/08/2021 02:00 PM TO 5:00 PM	SATURDAY	RESEARCH METHODOLOGY AND IPR [19MT10708]	RESEARCH METHODOLOGY AND IPR [19MT10708]	RESEARCH METHODOLOGY AND IPR [19MT10708]	RESEARCH METHODOLOGY AND IPR [19MT10708]

S. Sainath
14/7/2021
CHIEF CONTROLLER OF EXAMINATIONS
Y. Sainath

Copy to : The Director, Academics, SVEC.

The Vice-Principal, SVEC

The Dean, Academics

The Dean, Placements

The HODs, of EEE, ECE & CSE : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.

The Examination Section

The S.A.O, S.V.E.C

Hostel Notice Boards

The office, ISA.