

Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh
M.Tech II Semester (SVEC-16) Supplementary Examinations, January 2022

TIME - TABLE

Date : 07.12.2021

Time : 02:00 PM TO 05:00 PM

EXAM DATE & TIME	WEEK DAY	MTECH-CMS	MTECH-CNIS	MTECH-CS	MTECH-DECS	MTECH-EPS	MTECH-PED	MTECH-SE	MTECH-VLSI
06/01/2022 02:00PM TO 05:00PM	THURSDAY	DETECTION AND ESTIMATION OF SIGNALS [16MT23801]	INTRUSION DETECTION SYSTEMS [16MT26301]	ADVANCED COMPUTER ARCHITECTURE [16MT20501]	DETECTION AND ESTIMATION OF SIGNALS [16MT23801]	FLEXIBLE AC TRANSMISSION SYSTEM [16MT20701]	LINEAR AND NONLINEAR CONTROL SYSTEMS [16MT28301]	BIG DATA TECHNOLOGIES [16MT22501]	LOW POWER VLSI DESIGN [16MT25701]
08/01/2022 02:00PM TO 05:00PM	SATURDAY	ADAPTIVE SIGNAL PROCESSING [16MT26101]	BIG DATA ANALYTICS [16MT20502]	BIG DATA ANALYTICS [16MT20502]	EMBEDDED SYSTEM DESIGN [16MT23802]	INTELLIGENT SYSTEMS [16MT20702]	POWER ELECTRONICS IN RENEWABLE ENERGY SYSTEMS [16MT28302]	SERVICE ORIENTED ARCHITECTURE [16MT22502]	MIXED SIGNAL DESIGN [16MT25702]
11/01/2022 02:00PM TO 05:00PM	TUESDAY	IMAGE & VIDEO PROCESSING [16MT13804]	NETWORK PROGRAMMING [16MT26302]	OBJECT ORIENTED ANALYSIS AND DESIGN [16MT20503]	INFORMATION THEORY AND CODING TECHNIQUES [16MT23803]	POWER SYSTEM STABILITY AND CONTROL [16MT20703]	SOLID STATE AC DRIVES [16MT28303]	SOFTWARE ARCHITECTURE & DESIGN PATTERNS [16MT22503]	NANOELECTRONICS [16MT25703]
17/01/2022 02:00PM TO 05:00PM	MONDAY	SMART ANTENNAS [16MT26102]	CLOUD COMPUTING [16MT12501]	CLOUD COMPUTING [16MT12501]	LOW POWER CMOS VLSI DESIGN [16MT23804]	RESTRUCTURED POWER SYSTEM [16MT20704]	SOLID STATE DC DRIVES [16MT28304]	SOFTWARE TESTING TECHNIQUES [16MT22504]	PHYSICAL DESIGN AUTOMATION [16MT25704]
19/01/2022 02:00PM TO 05:00PM	WEDNESDAY	WIRELESS COMMUNICATIONS [16MT23805]	WIRELESS NETWORKS [16MT26303]	WEB TECHNOLOGIES [16MT22505]	WIRELESS COMMUNICATIONS [16MT23805]	STATIC AND DIGITAL PROTECTION OF POWER SYSTEM [16MT20705]	SPECIAL ELECTRICAL MACHINES [16MT28305]	WEB TECHNOLOGIES [16MT22505]	TESTING AND TESTABILITY [16MT25705]
21/01/2022 02:00PM TO 05:00PM	FRIDAY	RADAR SIGNAL PROCESSING [16MT26104] WIRELESS SENSOR NETWORKS [16MT25709]	COMPUTER FORENSICS [16MT26304]	INFORMATION SECURITY [16MT20505] MOBILE COMPUTING [16MT20506]	OPTICAL COMMUNICATIONS AND NETWORKS [16MT23807]	POWER QUALITY [16MT20708]	POWER QUALITY [16MT20708]	SOFTWARE PROCESS AND PROJECT MANAGEMENT [16MT22506]	ASIC DESIGN [16MT13806] SYSTEM-ON-CHIP DESIGN AND VERIFICATION [16MT25708]

Copy to : The Director, Academics & Research SVEC
The Vice-Principal, SVEC
The Dean, Academics, SVEC
The Dean, Placements,
The HODs, of EEE, ECE & CSE: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.
The Examination Section
The S.A.O, S.V.E.C
Hostel Notice Boards.
The Office, ISA.


CHIEF CONTROLLER OF EXAMINATIONS

