Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati,Chittoor,Andhra Pradesh

M.Tech II Semester (SVEC-19) Regular Examinations, January 2022

TIME - TABLE

Date : 07.12.2021

Time : 02:00 PM TO 05:00 PM

EXAM DATE & TIME	WEEK DAY	MTECH-CS	MTECH-VLSI
06/01/2022 02:00 PM TO 05:00PM	THURSDAY	DATA SCIENCE [19MT20501]	NANO MATERIALS AND NANOTECHNOLOGY [19MT25701]
08/01/2022 02:00 PM TO 05:00PM	SATURDAY	WEB TECHNOLOGIES [19MT20502]	PHYSICAL DESIGN AUTOMATION [19MT25702]
11/01/2022 02:00 PM TO 05:00PM	TUESDAY	COMPUTER VISION [19MT20504]	SYSTEM-ON-CHIP DESIGN [19MT25705]
17/01/2022 02:00 PM TO 05:00PM	MONDAY	SOFT COMPUTING [19MT20508]	NETWORK-ON-CHIP DESIGN [19MT25707]

CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, Academics & Research, SVEC
The Vice-Principal, SVEC
The Dean Academics, SVEC
The Dean, Placements,
The HODs, of ECE & CSE : with a request to arrange to read over in the respective
class rooms and circulate among the staff members
The Examination Section
The S.A.O, S.V.E.C
Hostel Notice Boards.
The Office, ISA.