

# SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

## IV B.Tech II Semester (SVEC-16) Supplementary Examinations, May 2022

### TIME - TABLE

Date : 23/04/2022

Time : 10:00 AM To 01:00 PM

EXAM DATE & TIME	WEEK DAY	CE	CSE	CSSE	ECE	EEE	EIE	IT	ME
30/05/2022 10:00 AM TO 01:00 PM	MONDAY	ENGINEERING HYDROLOGY [16BT40103]	BIG DATA ANALYTICS [16BT70501]	SOFTWARE PROJECT MANAGEMENT [16BT61503]	LINEAR IC APPLICATIONS [16BT40404]	COMPUTER ARCHITECTURE AND ORGANIZATION [16BT41041]	INDUSTRIAL INSTRUMENTATION - I [16BT41001]	LINUX PROGRAMMING [16BT50502]	PRODUCT DESIGN [16BT70310]
					COMPUTER NETWORKS [16BT50501]		SWITCHING THEORY AND LOGIC DESIGN (16BT30403)		
01/06/2022 10:00 AM TO 01:00 PM	WEDNESDAY	WATER SUPPLY ENGINEERING [16BT40105]	HUMAN COMPUTER INTERACTION [16BT70505]	MOBILE COMPUTING [16BT71204]	DIGITAL IC APPLICATIONS [16BT40402]	SPECIAL ELECTRICAL MACHINES [16BT60210]	LINEAR AND DIGITAL ICS [16BT41002]	INFORMATION RETRIEVAL SYSTEMS [16BT71203]	POWER PLANT ENGINEERING [16BT70312]
							ELECTRICAL TECHNOLOGY [16BT30241]		

  
CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, Academics & Research, SVEC.

The Vice-Principal, SVEC.

The Dean, Academics.

The Dean, Placements.

The HODs, of CE, ME, EEE, ECE, CSE, EIE, IT & CSSE: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.

The Examination Section.

The S.A.O, SVEC for information and necessary transport arrangement.

The Office, Valmar.

The Office, ISA.