

Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh
M.Tech II Semester (SVEC-16) Supplementary Examinations, October-2022

TIME - TABLE

Date : 16.09.2022

Time : 02:00 PM TO 05:00 PM

EXAM DATE & TIME	WEEK DAY	MTECH-CS	MTECH-EPS	MTECH-VLSI
25.10.2022 02:00PM TO 05:00PM	TUESDAY	ADVANCED COMPUTER ARCHITECTURE [16MT20501]	FLEXIBLE AC TRANSMISSION SYSTEM [16MT20701]	LOW POWER VLSI DESIGN [16MT25701]
27.10.2022 02:00PM TO	THURSDAY	BIG DATA ANALYTICS [16MT20502]	INTELLIGENT SYSTEMS [16MT20702]	MIXED SIGNAL DESIGN [16MT25702]
29.10.2022 02:00PM TO 05:00PM	SATURDAY	OBJECT ORIENTED ANALYSIS AND DESIGN [16MT20503]	POWER SYSTEM STABILITY AND CONTROL [16MT20703]	NANOELECTRONICS [16MT25703]
01.11.2022 02:00PM TO 05:00PM	TUESDAY	CLOUD COMPUTING [16MT12501]	RESTRUCTURED POWER SYSTEM [16MT20704]	PHYSICAL DESIGN AUTOMATION [16MT25704]
03.11.2022 02:00PM TO 05:00PM	THURSDAY	WEB TECHNOLOGIES [16MT22505]	STATIC AND DIGITAL PROTECTION OF POWER SYSTEM [16MT20705]	TESTING AND TESTABILITY [16MT25705]
05.11.2022 02:00PM TO 05:00PM	SATURDAY	INFORMATION SECURITY [16MT20505],	POWER QUALITY [16MT20708]	ASIC DESIGN [16MT13806]


CHIEF CONTROLLER OF EXAMINATIONS


Copy to : The Director, Academics & Research, SVEC.

The Vice-Principal, SVEC.

The Dean, Academics,

The Dean, Placements,

The HOD of CSE, EEE & ECE : with a request to arrange to read over in
the respective class rooms and circulate among the staff members
and display on notice Board.

The Examination Section.

The S.A.O, SVEC.

Hostel Notice Boards.

The Office, ISA.