

Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh
M.Tech I Semester (SVEC-16) Supplementary Examinations, October- 2022

TIME - TABLE

Date : 16.09.2022

Time : 02:00 PM TO 05:00 PM

EXAM DATE & TIME	WEEK DAY	MTECH-CS	MTECH-EPS	MTECH-VLSI
26.10.2022 2:00 PM TO 5:00 PM	WEDNESDAY	ADVANCED COMPUTER NETWORKS [16MT10501]	ADVANCED CONTROL SYSTEMS [16MT10701]	ANALOG IC DESIGN [16MT15701]
28.10.2022 2:00 PM TO 5:00 PM	FRIDAY	ADVANCED DATABASE MANAGEMENT SYSTEMS [16MT10502]	HIGH VOLTAGE ENGINEERING [16MT10702]	COMPUTATIONAL METHODS IN MICROELECTRONICS [16MT15702]
31.10.2022 2:00 PM TO 5:00 PM	MONDAY	ADVANCED OPERATING SYSTEMS [16MT10503]	POWER ELECTRONIC CONVERTERS [16MT10703]	DEVICE MODELING [16MT15703]
02.11.2022 2:00 PM TO 5:00 PM	WEDNESDAY	DATA WAREHOUSING AND DATA MINING [16MT10504]	POWER SYSTEM SECURITY AND STATE ESTIMATION [16MT10704]	DIGITAL IC DESIGN [16MT15704]
04.11.2022 2:00 PM TO 5:00 PM	FRIDAY	DATA STRUCTURES AND ALGORITHMS [16MT12502]	REACTIVE POWER COMPENSATION AND MANAGEMENT [16MT10705]	IC FABRICATION [16MT15705]
07.11.2022 2:00 PM TO 5:00 PM	MONDAY	SOFTWARE TESTING TECHNIQUES [16MT22504]	MICROCONTROLLERS AND APPLICATIONS [16MT10707],	FPGA ARCHITECTURES AND APPLICATIONS [16MT15707],


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, Academics & Research, SVEC.

The Vice-Principal, SVEC.

The Dean, Academics,

The Dean, Placements,

The HOD of CSE, EEE & ECE : with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.

The Examination Section.

The S.A.O, SVEC.

Hostel Notice Boards.

The Office, ISA.