

# Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

M.Tech II Semester (SVEC-19) Mid-II Examinations, October-2022


## TIME - TABLE

Date : 01.10.2022

Time : 03:00 PM TO 05:00 PM

EXAM DATE & TIME	WEEK DAY	MTECH-VLSI
17.10.2022 03:00 PM TO 05:00 PM	MONDAY	Nano Materials and Nanotechnology 19MT25701
18.10.2022 03:00 PM TO 05:00 PM	TUESDAY	Physical Design Automation 19MT25702
19.10.2022 03:00 PM TO 05:00 PM	WEDNESDAY	System-on-Chip Design 19MT25705
20.10.2022 03:00 PM TO 05:00 PM	THURSDAY	Network-on-Chip Design 19MT25707

1. Students have to bring their College Identity Cards to the examination hall; otherwise they will not be permitted to write the examination.
2. Examination will be on the Units prescribed in the Syllabus Book.
3. College buses will leave the college with in 15 minutes after Examination Time.

  
**PRINCIPAL**  
Yana (initials)

- Copy to:
- 1) The Director, Academics & Research, SVEC
  - 2) The Vice-Principal, SVEC.
  - 3) The Dean Academics, SVEC.
  - 4) The Dean Placements & Training.
  - 5) The HOD of ECE with a request to arrange to read out in the respective class rooms and circulation among the staff and to display on notice board.
  - 6) The Examination Section.
  - 7) The S.A.O, for information and necessary transport arrangement.
  - 8) Hostel Notice Boards
  - 9) The office, ISA