

SREE VIDYANIKETHAN ENGINEERING COLLEGE (AUTONOMOUS)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

III B.Tech I Semester (SVEC-16) Supplementary Examinations, November 2022

TIME - TABLE

Date : 17/10/2022

Time : 02:00 PM To 05:00 PM

EXAM DATE & TIME	WEEK DAY	CE	CSE	CSSE	ECE	EEE	EIE	IT	ME
24/11/2022 02:00 PM To 05:00 PM	THURSDAY	IRRIGATION ENGINEERING [16BT50101]	MANAGEMENT SCIENCE [16BT5HS01]	MANAGEMENT SCIENCE [16BT5HS01]	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY [16BT3HS02]	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY [16BT3HS02]	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY [16BT3HS02]	COMPUTER GRAPHICS AND MULTIMEDIA [16BT51201]	DESIGN OF MACHINE ELEMENTS-II [16BT50301]
26/11/2022 02:00 PM To 05:00 PM	SATURDAY	REINFORCED CEMENT CONCRETE STRUCTURES [16BT50102]	COMPUTER NETWORKS [16BT50501]	COMPUTER NETWORKS [16BT50501]	ELECTRONIC MEASUREMENTS AND INSTRUMENTATION [16BT50404] COMPUTER NETWORKS [16BT50501] OBJECT ORIENTED PROGRAMMING [16BT51241]	POWER ELECTRONICS [16BT50202]	BIOMEDICAL INSTRUMENTATION [16BT51001]	COMPUTER NETWORKS [16BT50501]	INDUSTRIAL ENGINEERING AND MANAGEMENT [16BT50302]
29/11/2022 02:00 PM To 05:00 PM	TUESDAY	SOIL MECHANICS [16BT50103]	LINUX PROGRAMMING [16BT50502]	COMPILER DESIGN [16BT51501]	CONTROL SYSTEMS [16BT50201] ** PULSE AND DIGITAL CIRCUITS [16BT40406]	CONTROL SYSTEMS [16BT50201]	INDUSTRIAL INSTRUMENTATION - II [16BT51002]	COMPILER DESIGN [16BT51501]	METROLOGY AND MEASUREMENTS [16BT50303]
01/12/2022 02:00 PM To 05:00 PM	THURSDAY	STRUCTURAL ANALYSIS - II [16BT50104]	OBJECT ORIENTED ANALYSIS AND DESIGN [16BT51202]	MICRO PROCESSORS AND INTERFACING [16BT50442]	DIGITAL COMMUNICATIONS [16BT50401]	SYNCHRONOUS MACHINES [16BT50203]	COMPUTER ORGANIZATION AND ARCHITECTURE [16BT51004]	OBJECT ORIENTED ANALYSIS AND DESIGN [16BT51202]	REFRIGERATION AND AIR- CONDITIONING [16BT50304]

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03/12/2022 02:00 PM To 05:00 PM	SATURDAY	WASTEWATER TECHNOLOGY [16BT50105]	THEORY OF COMPUTATION [16BT41204]	SYSTEMS SOFTWARE [16BT51502]	MICROPROCESSORS AND MICROCONTROLLERS [16BT50402]	TRANSMISSION AND DISTRIBUTION [16BT50204]	DIGITAL SIGNAL PROCESSING [16BT60402]	WEB TECHNOLOGIES [16BT51203]	THERMAL ENGINEERING-II [16BT50305]
06/12/2022 02:00 PM To 05:00 PM	TUESDAY	COSTING AND FINANCE MANAGEMENT FOR CIVIL ENGINEERS [16BT5HS02] RENEWABLE ENERGY [16BT50241]	COMPUTER VISION [16BT50503] DATA COMMUNICATIONS [16BT50504] MODELING AND SIMULATION [16BT51541]	PYTHON PROGRAMMING [16BT30503] INTELLIGENT COMPUTING SYSTEMS [16BT51503]	VLSI DESIGN [16BT50403]	DATABASE MANAGEMENT SYSTEMS [16BT40502] PRINCIPLES OF COMMUNICATIONS [16BT51003] SENSORS AND SIGNAL CONDITIONING [16BT51041]	PRINCIPLES OF COMMUNICATIONS [16BT51003] WIRELESS NETWORKS [16BT60503]	IMAGE PROCESSING [16BT60404] MECHATRONICS [16BT50308]	HUMAN RESOURCE MANAGEMENT [16BT50306] INSTRUMENTATION AND CONTROL SYSTEMS [16BT50307]

**** Substitute Subject**

Copy to : The Director, Academics and Research, SVEC.
The Vice-Principal, SVEC.
The Dean, Academics.
The Dean, Placements.

The HODs, of CE, EEE, ME, ECE, CSE, EIE, IT, CSSE and BS&H: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.

The Examination Section.
The S.A.O, SVEC for information.
The Office, Valmar for necessary transport and dining arrangements.
The Vice President, Administration for information.


CHIEF CONTROLLER OF EXAMINATIONS

