

## Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

II B.Tech I Semester (SVEC-19) Supplementary Examinations, November 2022

### TIME - TABLE

Date : 17/10/2022

Time : 10:00 AM To 01:00 PM

EXAM DATE & TIME	WEEK DAY	CE	CSE	CSSE	ECE	EEE	EIE	IT	ME
23/11/2022 10:00 AM To 01:00 PM	WEDNESDAY	Numerical Methods, Probability and Statistics 19BT3BS01	Numerical Methods, Probability and Statistics 19BT3BS01	Numerical Methods, Probability and Statistics 19BT3BS01	Special Functions and Complex Analysis 19BT3BS02	Special Functions and Complex Analysis 19BT3BS02	Special Functions and Complex Analysis 19BT3BS02	Numerical Methods, Probability and Statistics 19BT3BS01	Numerical Methods, Probability and Statistics 19BT3BS01
25/11/2022 10:00 AM To 01:00 PM	FRIDAY	Construction, Planning and Project Management 19BT30101	Discrete Mathematical Structures 19BT31201	Discrete Mathematical Structures 19BT31201	Electromagnetic Fields and Transmission Lines 19BT30401	Electrical Machines -1 19BT30201	Electrical and Electronic Measurements 19BT31001	Discrete Mathematical Structures 19BT31201	Engineering Thermodynamics 19BT30301
28/11/2022 10:00 AM To 01:00 PM	MONDAY	Fluid Mechanics 19BT30102	Operating Systems 19BT31502	Operating Systems 19BT31502	Electronic Devices and Circuits 19BT30402	Electronic Devices and Circuits 19BT30402 ** Programming for Problem Solving 19BT10501	Electronic Devices and Circuits 19BT30402	Software Engineering 19BT31202	Kinematics of Machinery 19BT30302
30/11/2022 10:00 AM To 01:00 PM	WEDNESDAY	Mechanics of Solids 19BT30103	Computer Graphics 19BT30501	Data Structures and Algorithms 19BT31501	Signals and Systems 19BT30403	Electromagnetic Fields 19BT30202	Transducers in Instrumentation 19BT31002	Computer Graphics 19BT30501	Manufacturing Technology 19BT30303
02/12/2022 10:00 AM To 01:00 PM	FRIDAY	Surveying 19BT30104	Computer Organization 19BT30502	Computer Organization 19BT30502	Switching Theory and Logic Design 19BT30404	Signals and Networks 19BT30203	Switching Theory and Logic Design 19BT30404	Computer Organization 19BT30502	Strength of Materials 19BT30304
05/12/2022 10:00 AM To 01:00 PM	MONDAY	--	Data Structures 19BT30503	System Programming 19BT31503	--	--	--	Data Structures 19BT30503	--

\*\* Substitute Subject

**CHIEF CONTROLLER OF EXAMINATIONS**

Copy to : The Director, Academics and Research, SVEC.

The Vice-Principal, SVEC.

The Dean, Academics.

The Dean, Placements.

The HODs, of CE, EEE, ME, ECE, CSE, EIE, IT, CSSE and BS&H: with a request to arrange to read over in the respective class rooms and The Examination Section.

The S.A.O, SVEC for information.

The Office, Valmar for necessary transport and dining arrangements.

The Vice President, Administration for information.

