

Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

II B.Tech I Semester (SVEC-16) Supplementary Examinations, November 2022

TIME - TABLE

Date : 17/10/2022

Time : 10:00 AM To 01:00 PM

EXAM DATE & TIME	WEEK DAY	CE	CSE	CSSE	ECE	EEE	EIE	IT	ME
23/11/2022 10:00 AM To 01:00 PM	WEDNESDAY	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]
25/11/2022 10:00 AM To 01:00 PM	FRIDAY	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY [16BT3HS02]	COMPUTER ORGANIZATION [16BT30501]	COMPUTER ORGANIZATION [16BT30501]	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS [16BT3BS02]	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS [16BT3BS02]	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS [16BT3BS02]	COMPUTER ORGANIZATION [16BT30501]	ENGINEERING METALLURGY [16BT30301]
28/11/2022 10:00 AM To 01:00 PM	MONDAY	CONSTRUCTION PLANNING AND PROJECT MANAGEMENT [16BT30101]	DATA STRUCTURES [16BT30502]	DATA STRUCTURES [16BT30502]	ELECTRONIC CIRCUIT ANALYSIS & DESIGN [16BT30401]	DC MACHINES [16BT30201]	ELECTRICAL AND ELECTRONIC MEASUREMENTS [16BT31001]	DATA STRUCTURES [16BT30502]	KINEMATICS OF MACHINERY [16BT30302]
30/11/2022 10:00 AM To 01:00 PM	WEDNESDAY	FLUID MECHANICS AND HYDRAULIC MACHINERY [16BT30102]	DISCRETE MATHEMATICAL STRUCTURES [16BT31201]	DISCRETE MATHEMATICAL STRUCTURES [16BT31201]	SIGNALS AND SYSTEMS [16BT30402]	ELECTROMAGNETIC FIELDS [16BT30202]	SENSORS AND TRANSDUCERS [16BT31002]	DISCRETE MATHEMATICAL STRUCTURES [16BT31201]	MANUFACTURING TECHNOLOGY [16BT30303]
02/12/2022 10:00 AM To 01:00 PM	FRIDAY	MECHANICS OF SOLIDS [16BT30103]	PYTHON PROGRAMMING [16BT30503]	JAVA PROGRAMMING [16BT41202]	SWITCHING THEORY AND LOGIC DESIGN [16BT30403]	SIGNALS, SYSTEMS AND NETWORKS [16BT30203]	SWITCHING THEORY AND LOGIC DESIGN [16BT30403]	LINUX PROGRAMMING [16BT50502]	STRENGTH OF MATERIALS [16BT30304]
05/12/2022 10:00 AM To 01:00 PM	MONDAY	SURVEYING [16BT30104]	OPERATING SYSTEMS [16BT31501]	OPERATING SYSTEMS [16BT31501]	ELECTRICAL TECHNOLOGY [16BT30241]	ANALOG ELECTRONIC CIRCUITS [16BT30441]	ELECTRICAL TECHNOLOGY [16BT30241]	OPERATING SYSTEMS [16BT31501]	THERMODYNAMICS [16BT30305]


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, Academics and Research, SVEC.

The Vice-Principal, SVEC.

The Dean, Academics.

The Dean, Placements.

The HODs, of CE, EEE, ME, ECE, CSE, EIE, IT, CSSE and BS&H: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.

The Examination Section.

The S.A.O, SVEC for information.

The Office, Valmar for necessary transport and dining arrangements.

The Vice President, Administration for information.