

Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

II B.Tech II Semester (SVEC-14) Supplementary Examinations, December 2022

TIME - TABLE

Date : 17/10/2022

Time : 02:00 PM To 05:00 PM

EXAM DATE & TIME	WEEK DAY	CE	CSE	CSSE	ECE	EEE	EIE	IT	ME
07/12/2022 02:00 PM To 05:00 PM	WEDNESDAY	BUSINESS COMMUNICATION AND PRESENTATION SKILLS [14BT4HS01]	BUSINESS COMMUNICATION AND PRESENTATION SKILLS [14BT4HS01]	BUSINESS COMMUNICATION AND PRESENTATION SKILLS [14BT4HS01]	PROFESSIONAL ETHICS [14BT4HS02]	PROFESSIONAL ETHICS [14BT4HS02]	PROFESSIONAL ETHICS [14BT4HS02]	BUSINESS COMMUNICATION AND PRESENTATION SKILLS [14BT4HS01]	BUSINESS COMMUNICATION AND PRESENTATION SKILLS [14BT4HS01]
09/12/2022 02:00 PM To 05:00 PM	FRIDAY	ENVIRONMENTAL SCIENCES [14BT3HS01]	ENVIRONMENTAL SCIENCES [14BT3HS01]	ENVIRONMENTAL SCIENCES [14BT3HS01]	ANALOG COMMUNICATIONS [14BT40401]	SIGNALS AND NETWORKS [14BT40201]	ELECTRICAL TECHNOLOGY [14BT30231]	ENVIRONMENTAL SCIENCES [14BT3HS01]	ENVIRONMENTAL SCIENCES [14BT3HS01]
12/12/2022 02:00 PM To 05:00 PM	MONDAY	PROBABILITY & STATISTICS [14BT3BS03]	COMPUTER ORGANIZATION [14BT40501]	COMPUTER ORGANIZATION [14BT40501]	ELECTRONIC CIRCUIT ANALYSIS AND DESIGN [14BT40402]	GENERATION OF ELECTRIC POWER [14BT40202]	ELECTRONIC CIRCUIT ANALYSIS AND DESIGN [14BT40402]	COMPUTER ORGANIZATION [14BT40501]	PROBABILITY & STATISTICS [14BT3BS03]
14/12/2022 02:00 PM To 05:00 PM	WEDNESDAY	STRUCTURAL ANALYSIS - I [14BT40101]	DATABASE MANAGEMENT SYSTEMS [14BT40502]	DATABASE MANAGEMENT SYSTEMS [14BT40502]	ELECTROMAGNETIC THEORY AND TRANSMISSION LINES [14BT40403]	ELECTRICAL AND ELECTRONIC MEASUREMENTS [14BT40203]	PULSE AND DIGITAL CIRCUITS [14BT50405]	DATABASE MANAGEMENT SYSTEMS [14BT40502]	KINEMATICS OF MACHINERY [14BT40301]
16/12/2022 02:00 PM To 05:00 PM	FRIDAY	CONSTRUCTION, PLANNING & PROJECT MANAGEMENT [14BT40102]	DESIGN AND ANALYSIS OF ALGORITHMS [14BT40503]	THEORY OF COMPUTATION [14BT50501]	SIGNALS AND SYSTEMS [14BT40404]	TRANSFORMERS AND INDUCTION MACHINES [14BT40204]	SIGNALS AND SYSTEMS [14BT40404]	OPERATING SYSTEMS [14BT51501]	FLUID MECHANICS AND HYDRAULIC MACHINERY [14BT40302]
19/12/2022 02:00 PM To 05:00 PM	MONDAY	SURVEYING [14BT40103]	OBJECT ORIENTED PROGRAMMING [14BT41201]	OBJECT ORIENTED PROGRAMMING [14BT41201]	SWITCHING THEORY AND LOGIC DESIGN [14BT40405]	SWITCHING THEORY AND LOGIC DESIGN [14BT40405]	SWITCHING THEORY AND LOGIC DESIGN [14BT40405]	OBJECT ORIENTED PROGRAMMING [14BT41201]	THERMAL ENGINEERING-I [14BT40303]
21/12/2022 02:00 PM To 05:00 PM	WEDNESDAY	FLUID MECHANICS - II [14BT40104]	COMPUTER GRAPHICS [14BT41501]	COMPUTER GRAPHICS [14BT41501]	CONTROL SYSTEMS [14BT50201]	ANALOG ELECTRONIC CIRCUITS [14BT40431]	CONTROL SYSTEMS [14BT50201]	DATA COMMUNICATIONS [14BT40432]	MANUFACTURING TECHNOLOGY-II [14BT40304]


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, Academics and Research, SVEC.

The Vice-Principal, SVEC.

The Dean, Academics.

The Dean, Placements.

The HODs, of CE, EEE, ME, ECE, CSE, EIE, IT, CSSE and BS&H: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.

The Examination Section.

The S.A.O, SVEC for information.

The Office, Valmar for necessary transport and dining arrangements.

The Vice President, Administration for information.