

Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

I B.Tech II Semester (SVEC-16) Supplementary Examinations, June 2023

TIME - TABLE

Date : 12/05/2023

Time : 02:00 PM To 05:00 PM

EXAM DATE & TIME	WEEK DAY	CE	CSE	CSSE	ECE	EEE	EIE	IT	ME
13/06/2023 02:00 PM To 05:00 PM	TUESDAY	ENGINEERING PHYSICS [16BT1BS02]	ENGINEERING PHYSICS [16BT1BS02]	ENGINEERING PHYSICS [16BT1BS02]	TECHNICAL ENGLISH [16BT1HS01]	TECHNICAL ENGLISH [16BT1HS01]	TECHNICAL ENGLISH [16BT1HS01]	ENGINEERING PHYSICS [16BT1BS02]	ENGINEERING PHYSICS [16BT1BS02]
15/06/2023 02:00 PM To 05:00 PM	THURSDAY	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS [16BT2BS01]
17/06/2023 02:00 PM To 05:00 PM	SATURDAY	BUILDING MATERIAL AND CONSTRUCTION TECHNOLOGY [16BT20101]	BASIC ELECTRONIC DEVICES AND CIRCUITS [16BT20441]	BASIC ELECTRONIC DEVICES AND CIRCUITS [16BT20441]	ELECTRONIC DEVICES AND CIRCUITS [16BT20401]	ELECTRONIC DEVICES AND CIRCUITS [16BT20401]	ELECTRONIC DEVICES AND CIRCUITS [16BT20401]	BASIC ELECTRONIC DEVICES AND CIRCUITS [16BT20441]	ENGINEERING MATERIALS [16BT20301]
20/06/2023 02:00 PM To 05:00 PM	TUESDAY	ENGINEERING MECHANICS [16BT20102]	OBJECT ORIENTED PROGRAMMING THROUGH C++ [16BT21201]	OBJECT ORIENTED PROGRAMMING THROUGH C++ [16BT21201]	ENGINEERING CHEMISTRY [16BT1BS01]	ENGINEERING CHEMISTRY [16BT1BS01]	ENGINEERING CHEMISTRY [16BT1BS01]	OBJECT ORIENTED PROGRAMMING THROUGH C++ [16BT21201]	ENGINEERING MECHANICS [16BT20102]
22/06/2023 02:00 PM To 05:00 PM	THURSDAY	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING [16BT20241]	DIGITAL LOGIC DESIGN [16BT21501]	DIGITAL LOGIC DESIGN [16BT21501]	FOUNDATIONS OF DATA STRUCTURES [16BT20541]	FOUNDATIONS OF DATA STRUCTURES [16BT20541]	FOUNDATIONS OF DATA STRUCTURES [16BT20541]	DIGITAL LOGIC DESIGN [16BT21501]	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING [16BT20241]


CHIEF CONTROLLER OF EXAMINATIONS

Copy to : The Director, Academics and Research, SVEC.

The Vice-Principal, SVEC.

The Dean, Academics.

The Dean, Placements.

The HODs, of CE, EEE, ME, ECE, CSE, EIE, IT, CSSE and BS&H: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.

The Examination Section.

The S.A.O, SVEC for information.