## Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati, Chittoor, Andhra Pradesh

## II B.Tech I Semester (SVEC-16) Supplementary Examinations, May 2024

TIME - TABLE

Date: 04/04/2024

Time: 02:00 PM To 05:00 PM

EXAM DATE								Time : 02:00 I	PM To 05:00 PM
& TIME	WEEK DAY	CE	CSE	CSSE	ECE	EEE	EIE	IT	ME
17/05/2024 02:00 PM To 05:00 PM	FRIDAY	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	ENVIRONMENTAL STUDIES [16BT3HS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]	PROBABILITY DISTRIBUTIONS AND STATISTICAL METHODS [16BT3BS01]
20/05/2024 02:00 PM To 05:00 PM	MONDAY	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY	COMPUTER ORGANIZATION	COMPUTER ORGANIZATION	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS	FUNCTIONS AND COMPLEX ANALYSIS	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS	COMPUTER ORGANIZATION	ENGINEERING METALLURGY
00/05/0004		[16BT3HS02] CONSTRUCTION	[16BT30501]	[16BT30501]	[16BT3BS02]	[16BT3BS02]	[16BT3BS02]	[16BT30501]	[16BT30301]
22/05/2024 02:00 PM To 05:00 PM	WEDNESDAY	PLANNING AND PROJECT MANAGEMENT			ELECTRONIC CIRCUIT ANALYSIS & DESIGN	DC MACHINES	ELECTRICAL AND ELECTRONIC MEASUREMENTS	DATA STRUCTURES	KINEMATICS OF MACHINERY
04/05/0004		[16BT30101]	[16BT30502]	[16BT30502]	[16BT30401]	[16BT30201]	[16BT31001]	[16BT30502]	[16BT30302]
24/05/2024 02:00 PM To 05:00 PM	FRIDAY	FLUID MECHANICS AND HYDRAULIC MACHINERY	DISCRETE MATHEMATICAL STRUCTURES	DISCRETE MATHEMATICAL STRUCTURES	SIGNALS AND SYSTEMS	ELECTROMAGNETIC FIELDS	SENSORS AND TRANSDUCERS	DISCRETE MATHEMATICAL STRUCTURES	MANUFACTURING TECHNOLOGY
		[16BT30102]	[16BT31201]	[16BT31201]	[16BT30402]	[16BT30202]	[16BT31002]	[16BT31201]	[16BT30303]
27/05/2024 02:00 PM To	MONDAY	MECHANICS OF SOLIDS	PYTHON PROGRAMMING	JAVA PROGRAMMING	SWITCHING THEORY AND LOGIC DESIGN	SIGNALS, SYSTEMS AND NETWORKS	SWITCHING THEORY AND LOGIC DESIGN	LINUX PROGRAMMING	STRENGTH OF MATERIALS
05:00 PM		[16BT30103]	[16BT30503]	[16BT41202]	[16BT30403]	[16BT30203]	[16BT30403]	[16BT50502]	[16BT30304]
29/05/2024 02:00 PM To 05:00 PM	WEDNESDAY	SURVEYING	OPERATING SYSTEMS	OPERATING SYSTEMS	ELECTRICAL TECHNOLOGY	ANALOG ELECTRONIC CIRCUITS	ELECTRICAL TECHNOLOGY	OPERATING SYSTEMS	THERMODYNAMICS
		[16BT30104]	[16BT31501]	[16BT31501]	[16BT30241]	[16BT30441]	[16BT30241]	[16BT31501]	[16BT30305]

Copy to: The Director, Academics and Research, SVEC.

The Vice-Principal, SVEC. The Dean, Academics. The Dean, Placements.

The HODs, of CE, EEE, ME, ECE, CSE, EIE, IT, CSSE and BS&H: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.

The Examination Section.

The S.A.O, SVEC for information.

CHIEF CONTROLLER OF EXAMINATIONS

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