

Sree Vidyanikethan Engineering College (Autonomous)

Sree Sainath Nagar, A. Rangampet, Tirupati,Chittoor,Andhra Pradesh

I B.Tech II Semester (SVEC-20) Supplementary Examinations, April 2025

TIME - TABLE

Date:15/03/2025

EXAM DATE & TIME	WEEK DAY	CE	CSD	CSBS	CSE	CSE (AI)	CSE (DS)	CSE (CS)	CSE (AI&ML)	CSE (IoT)	CSSE	ECE	EEE	EIE	IT	ME
<b>16/04/2025</b> 02:00 PM To 05:00 PM	WEDNESDAY	Transformation Techniques and Linear Algebra  20BT2BS01	Transformation Techniques and Linear Algebra  20BT2BS01	Transformation Techniques and Linear Algebra  20BT2BS01	Transformation Techniques and Linear Algebra  20BT2BS01	Transformation Techniques and Linear Algebra  20BT2BS01	Transformation Techniques and Linear Algebra  20BT2BS01	Transformation Techniques and Linear Algebra  20BT2BS01	Transformation Techniques and Linear Algebra  20BT2BS01	Transformation Techniques and Linear Algebra  20BT2BS01	Transformation Techniques and Linear Algebra  20BT2BS01	Transformation Techniques and Linear Algebra  20BT2BS01	Transformation Techniques and Linear Algebra  20BT2BS01	Transformation Techniques and Linear Algebra  20BT2BS01	Transformation Techniques and Linear Algebra  20BT2BS01	Transformation Techniques and Linear Algebra  20BT2BS01
<b>19/04/2025</b> 02:00 PM To 05:00 PM	SATURDAY	Civil Engineering Materials and Concrete Technology  20BT20101	Engineering Physics  20BT1BS03	Engineering Physics  20BT1BS03	Engineering Physics  20BT1BS03	Engineering Chemistry  20BT1BS02	Engineering Chemistry  20BT1BS02	Engineering Physics  20BT1BS03	Engineering Chemistry  20BT1BS02	Engineering Physics  20BT1BS03	Engineering Physics  20BT1BS03	Engineering Chemistry  20BT1BS02	Engineering Chemistry  20BT1BS02	Engineering Chemistry  20BT1BS02	Engineering Physics  20BT1BS03	Applied Physics  20BT1BS04
<b>22/04/2025</b> 02:00 PM To 05:00 PM	TUESDAY	Basic Electrical and Electronics Engineering  20BT10201	Digital Logic Design  20BT20501	Basic Electrical and Electronics Engineering  20BT10201	Digital Logic Design  20BT20501	Digital Logic Design  20BT20501	Digital Logic Design  20BT20501	Digital Logic Design  20BT20501	Digital Logic Design  20BT20501	Digital Logic Design  20BT20501	Digital Logic Design  20BT20501	Network Analysis  20BT20241	Electrical Circuits  20BT20201	Network Analysis  20BT20241	Digital Logic Design  20BT20501	Basic Engineering Mechanics  20BT20301
<b>24/04/2025</b> 02:00 PM To 05:00 PM	THURSDAY	Programming in C and Data Structures  20BT20541	Object Oriented Programming through Java  20BT21501	Object Oriented Programming through Java  20BT21501	Object Oriented Programming through Java  20BT21501	Object Oriented Programming through Java  20BT21501	Object Oriented Programming through Java  20BT21501	Object Oriented Programming through Java  20BT21501	Object Oriented Programming through Java  20BT21501	Object Oriented Programming through Java  20BT21501	Object Oriented Programming through Java  20BT21501	Programming in C and Data Structures  20BT20541	Programming in C and Data Structures  20BT20541	Programming in C and Data Structures  20BT20541	Object Oriented Programming through Java  20BT21501	Programming in C and Data Structures  20BT20541
<b>26/04/2025</b> 02:00 PM To 05:00 PM	SATURDAY	Communicative English  20BT1HS01	Communicative English  20BT1HS01	Data Structures and Algorithms  20BT22901	--	--	--	--	--	Communicative English  20BT1HS01	--	Communicative English  20BT1HS01	Communicative English  20BT1HS01	Communicative English  20BT1HS01	--	Communicative English  20BT1HS01

**CHIEF CONTROLLER OF EXAMINATIONS**

Copy to : The Dean, Academics.  
The Dean, Placements.  
The HODs, of **CE, EEE, ME, ECE, CSE, EIE, IT, CSSE and BS&H**: with a request to arrange to read over in the respective class rooms and circulate among the staff members and display on notice Board.  
The Examination Section.  
The S.A.O, SVEC for information.